

INPUT PORTION
FOR POWER
SUPPLY LINES 2

INPUT PORTION
FOR POWER
SUPPLY LINES 1

FIG. 1



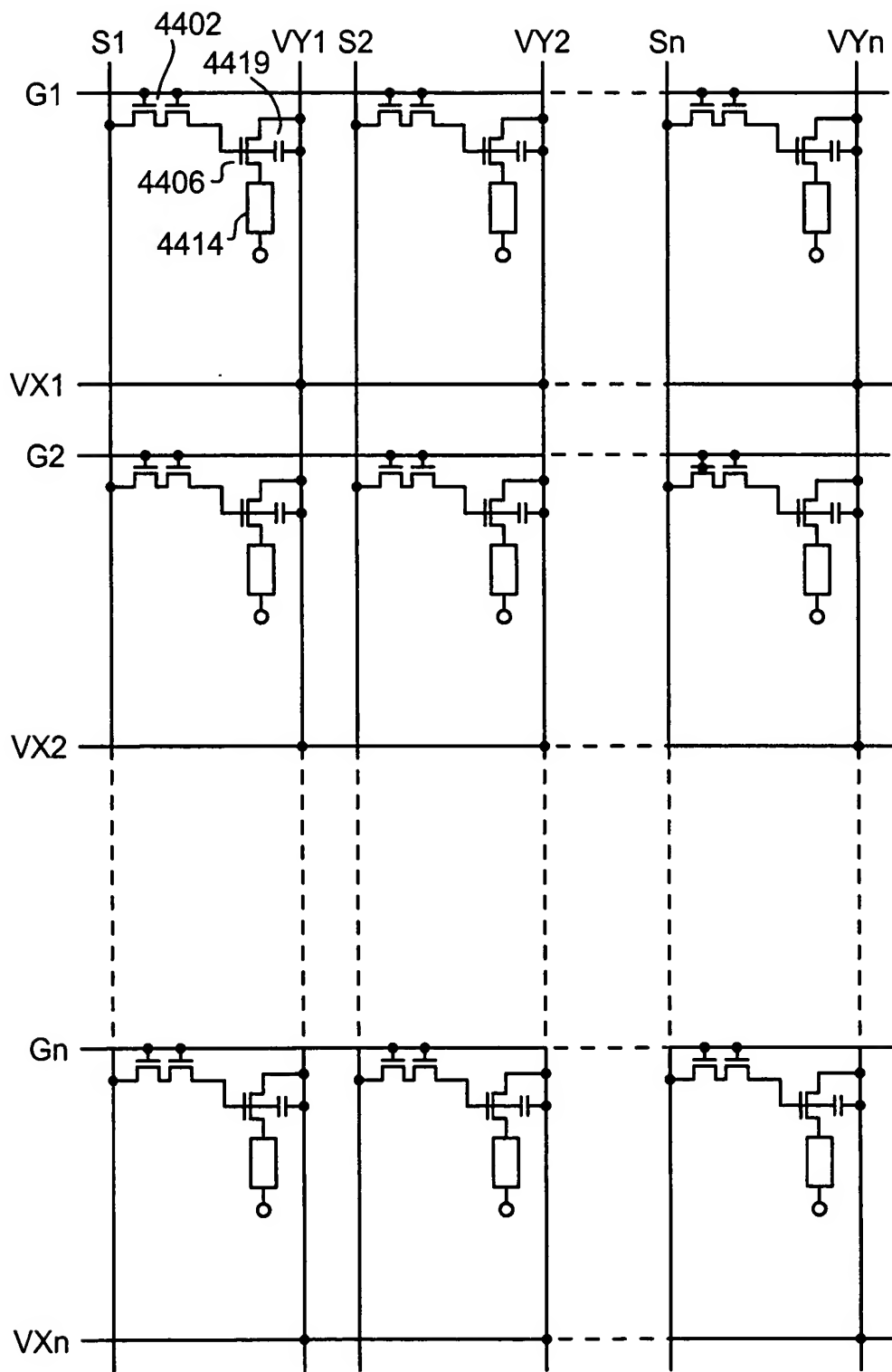


FIG. 2

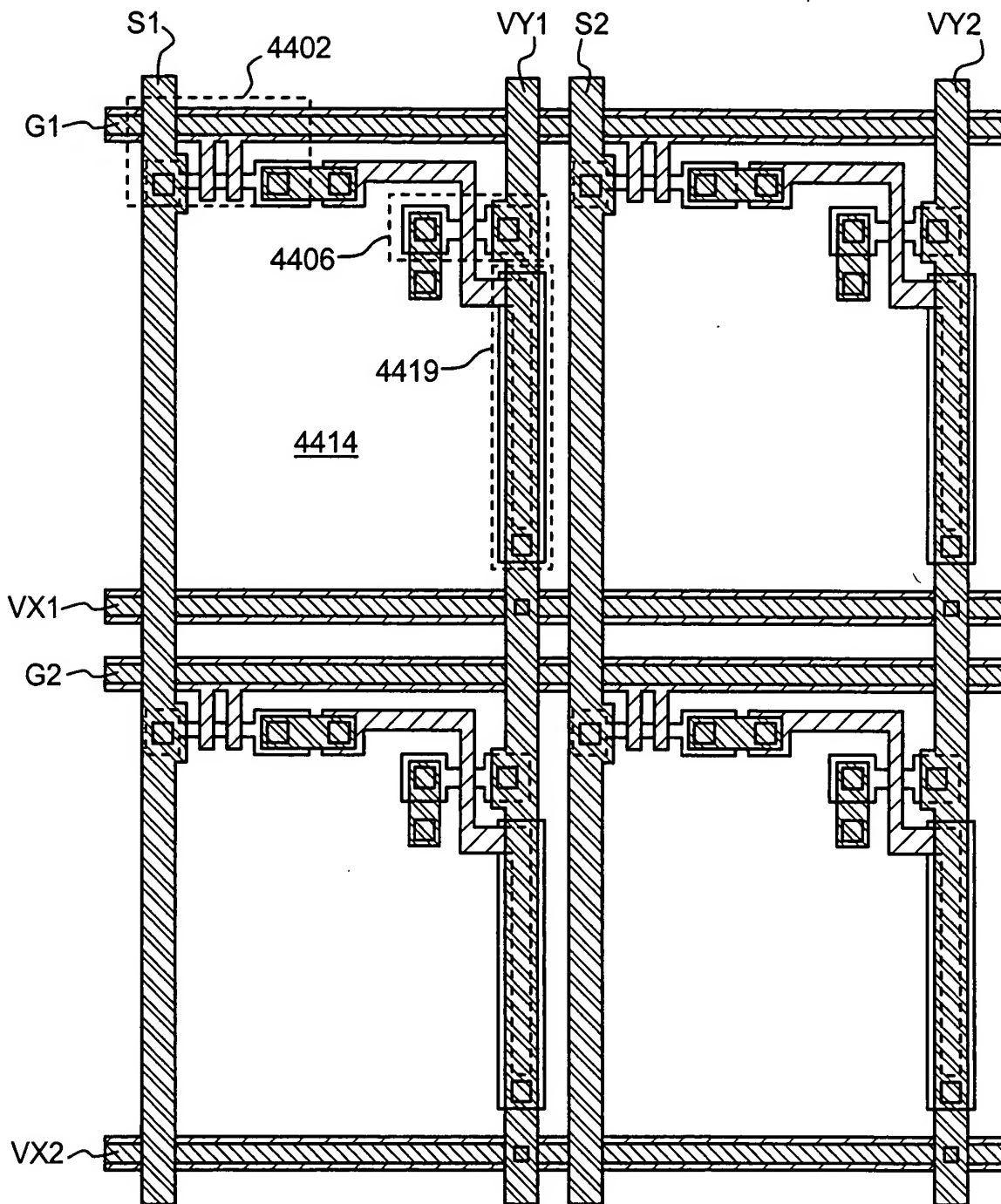


FIG. 3

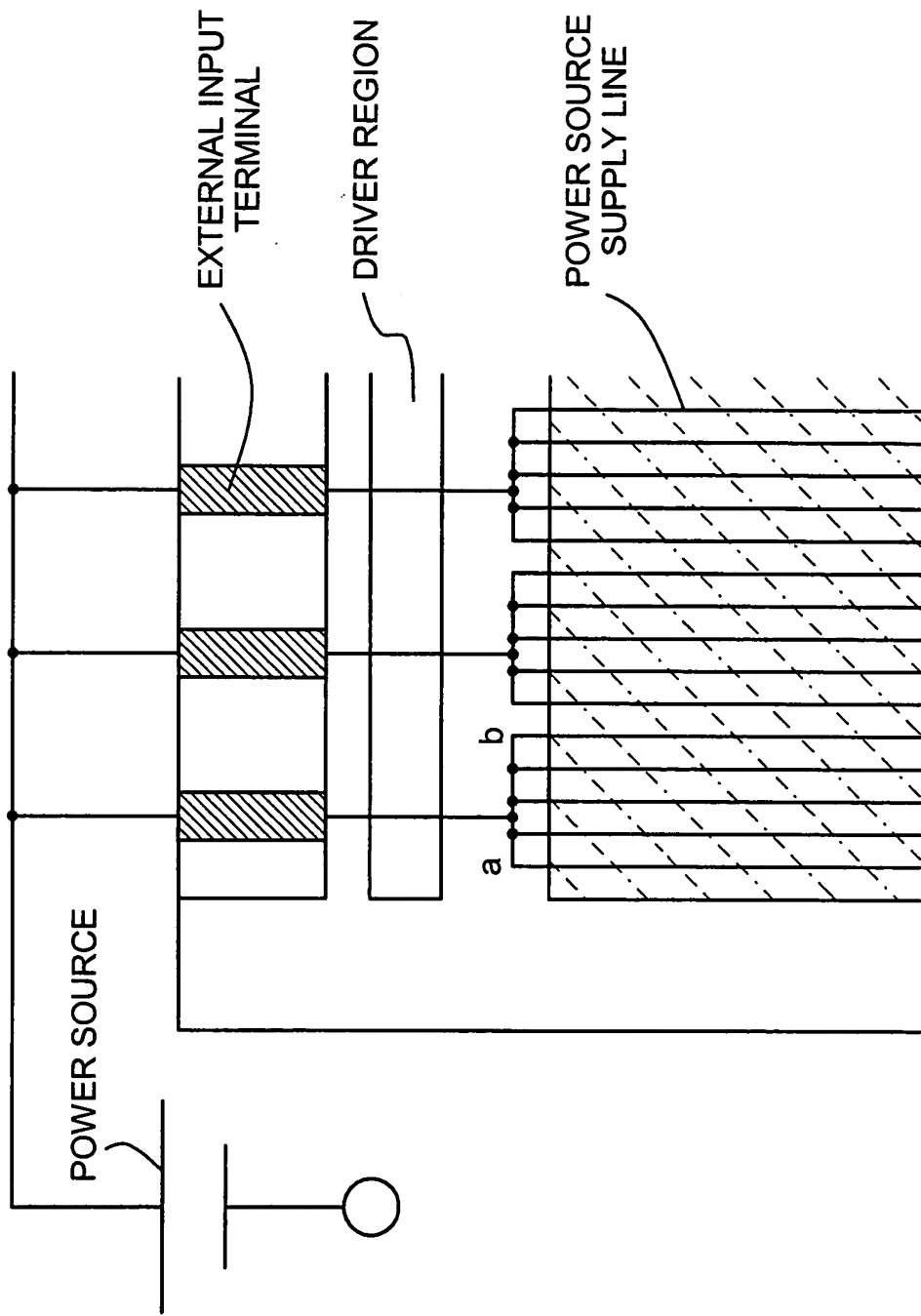


FIG. 4

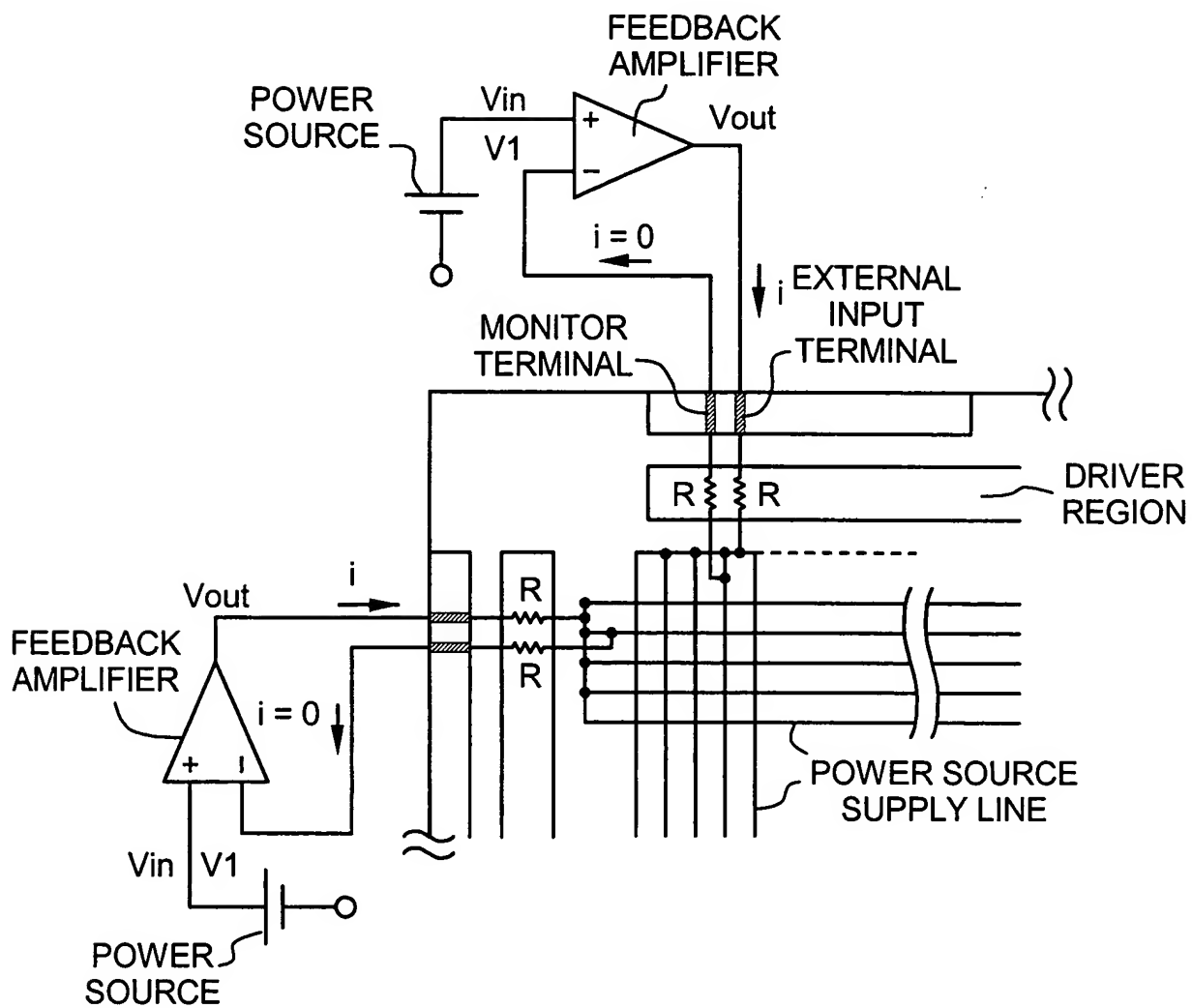


FIG. 5

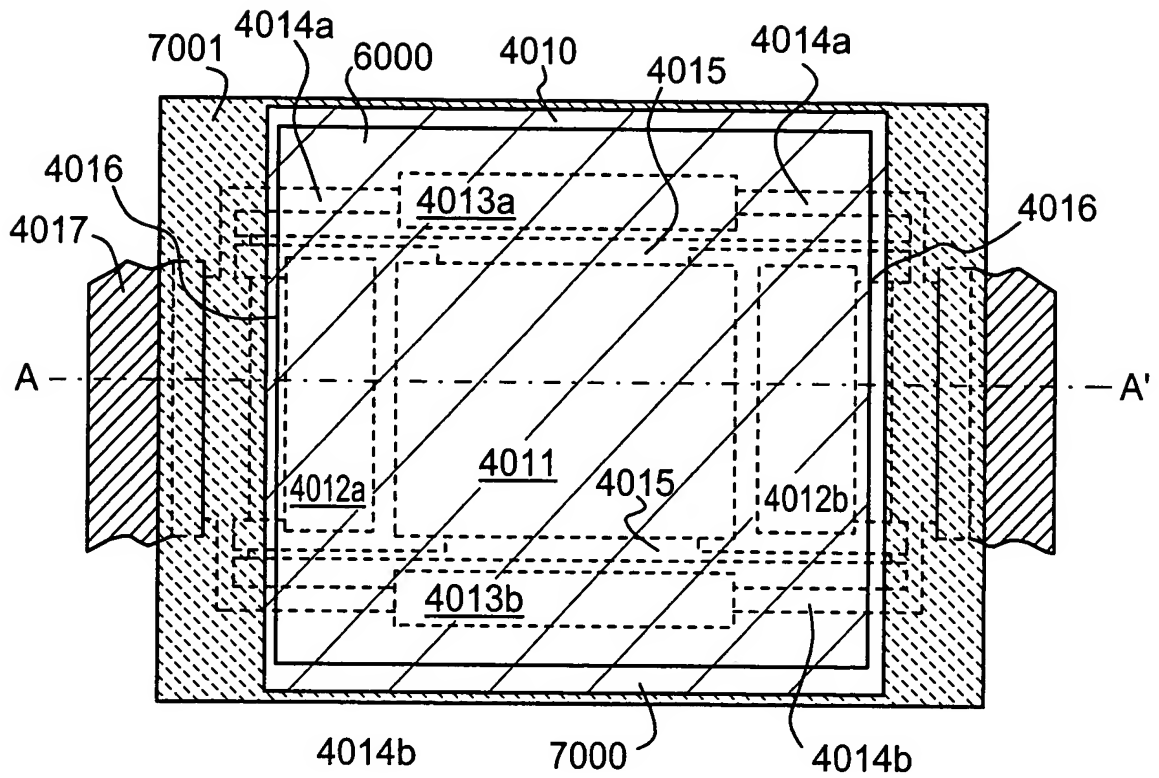


FIG. 6A

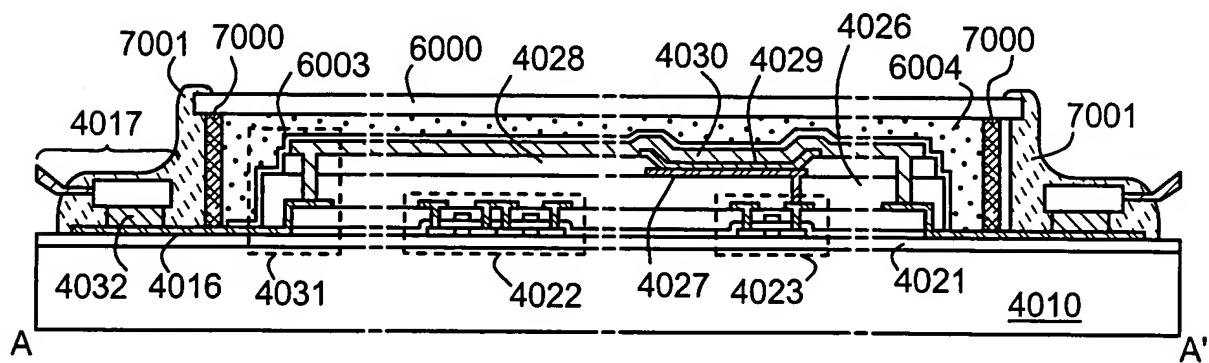


FIG. 6B

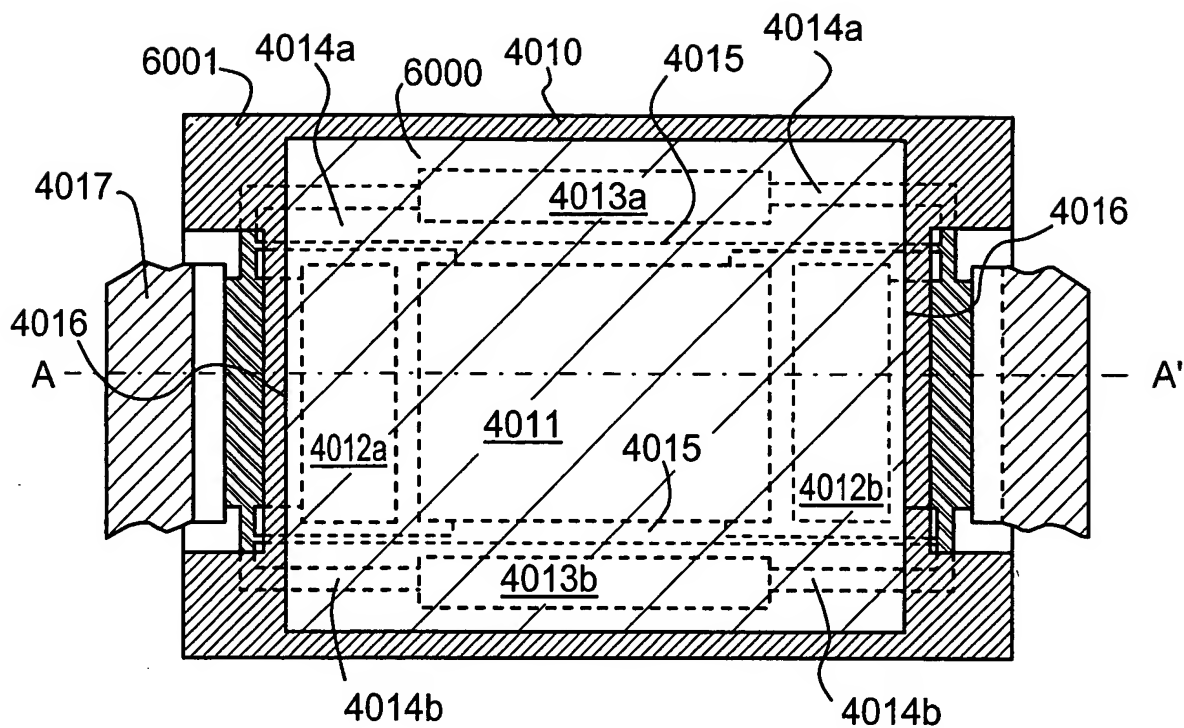


FIG. 7A

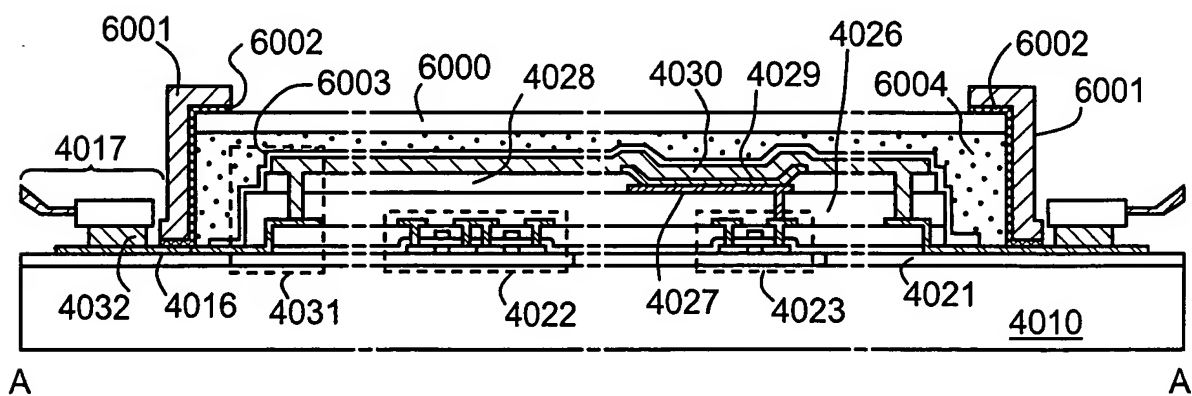


FIG. 7B

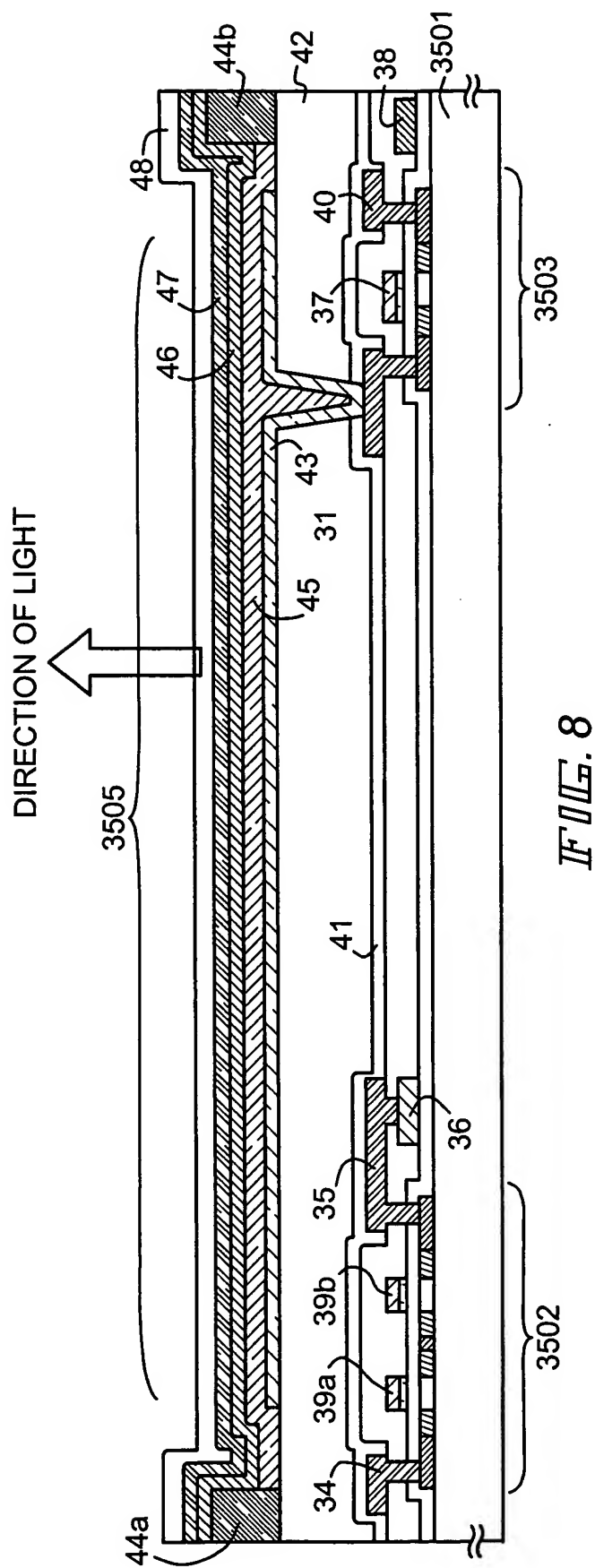


FIG. 8

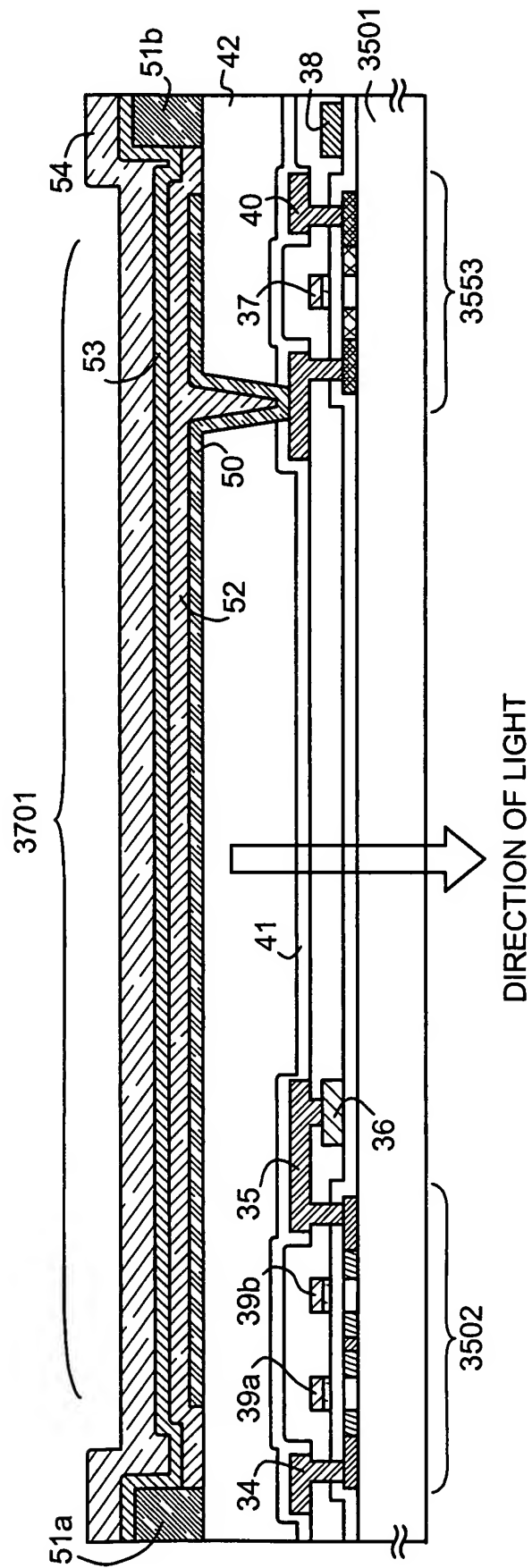


FIG. 9

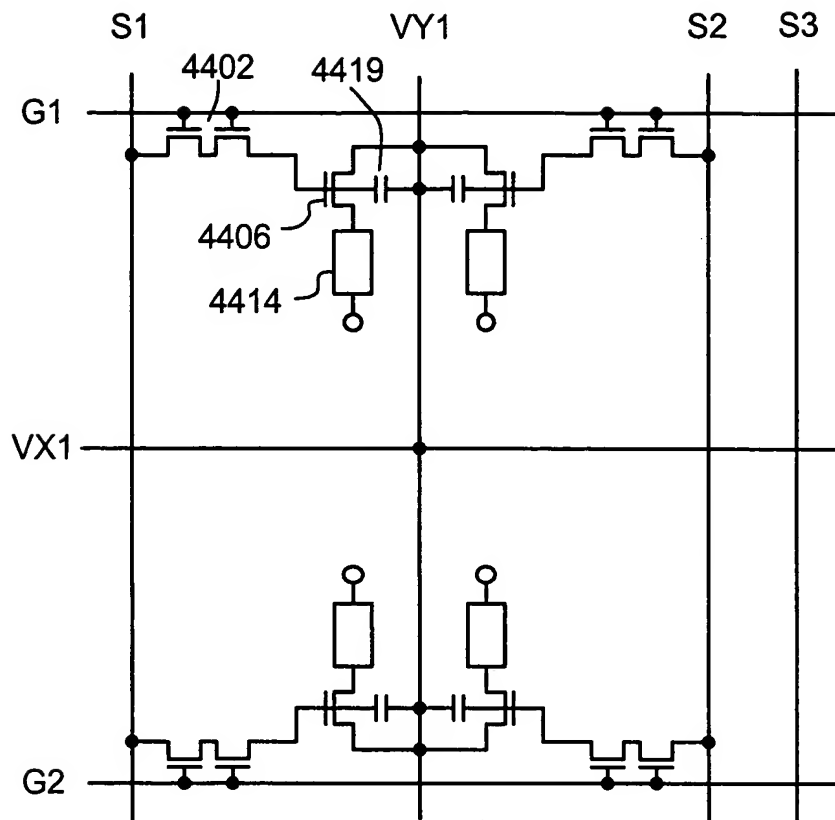


FIG. 10

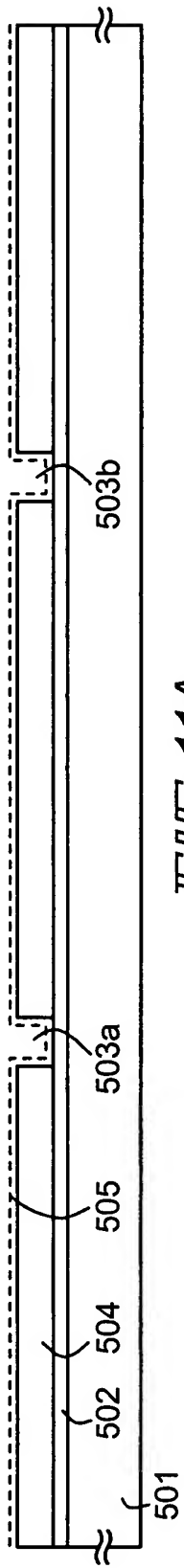


FIG. 11A



FIG. 11B

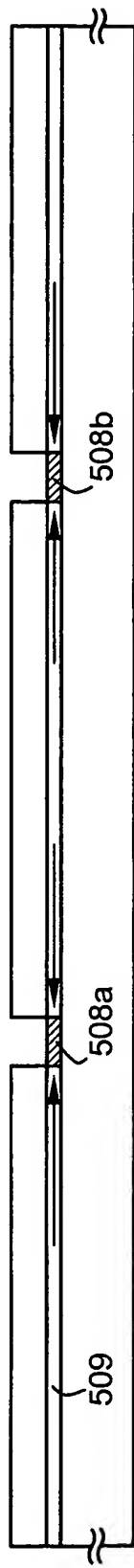


FIG. 11C



FIG. 11D

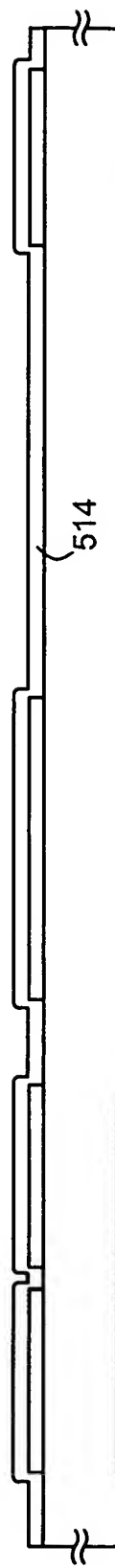


FIG. 11E



FIG. 12A

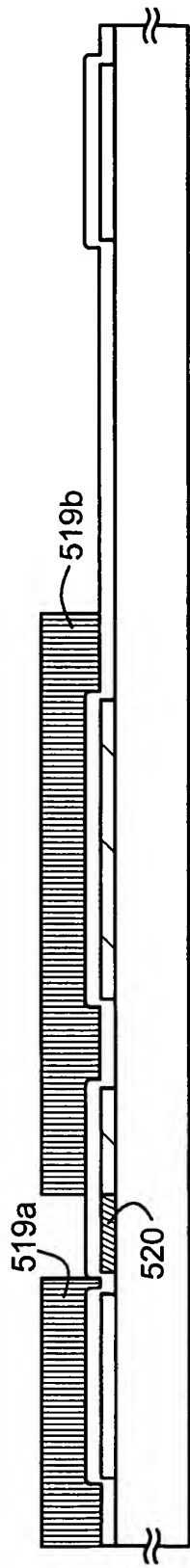


FIG. 12B



FIG. 12C

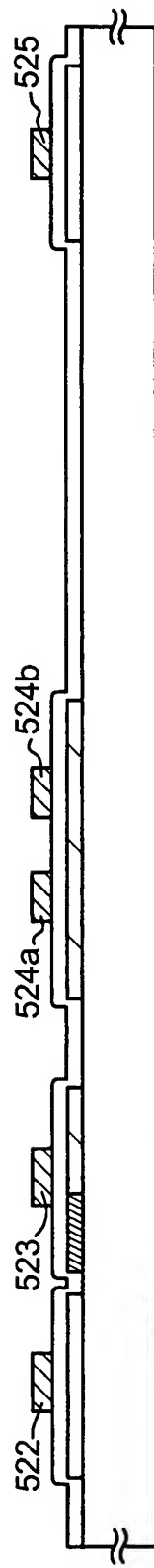
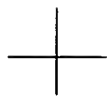


FIG. 12D



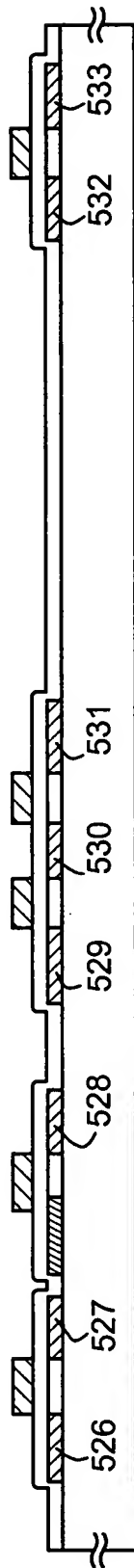


FIG. 13A

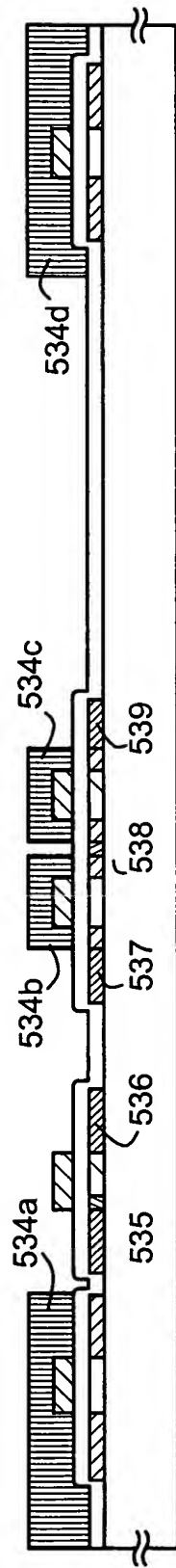


FIG. 13B

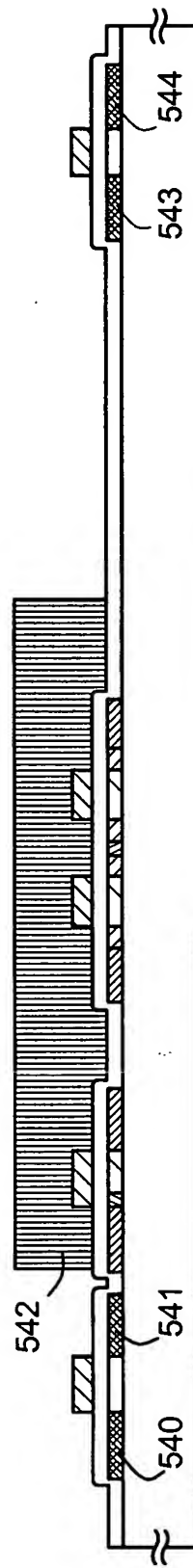


FIG. 13C

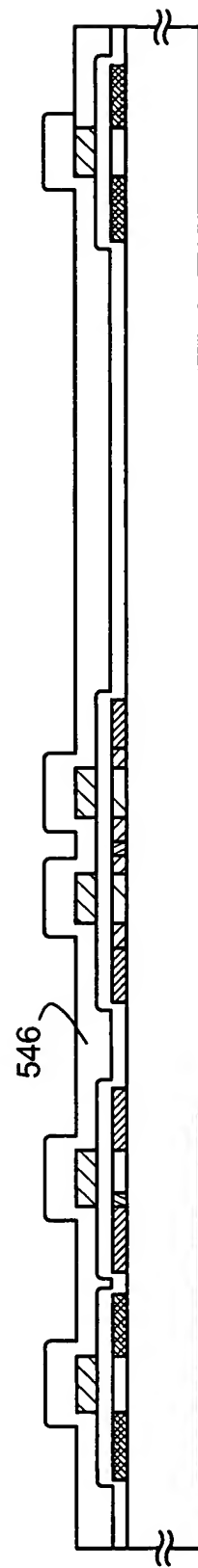


FIG. 13D

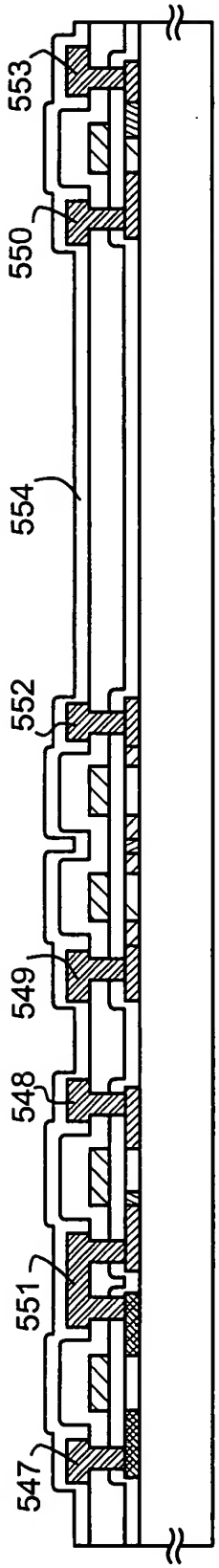


FIG. 14A

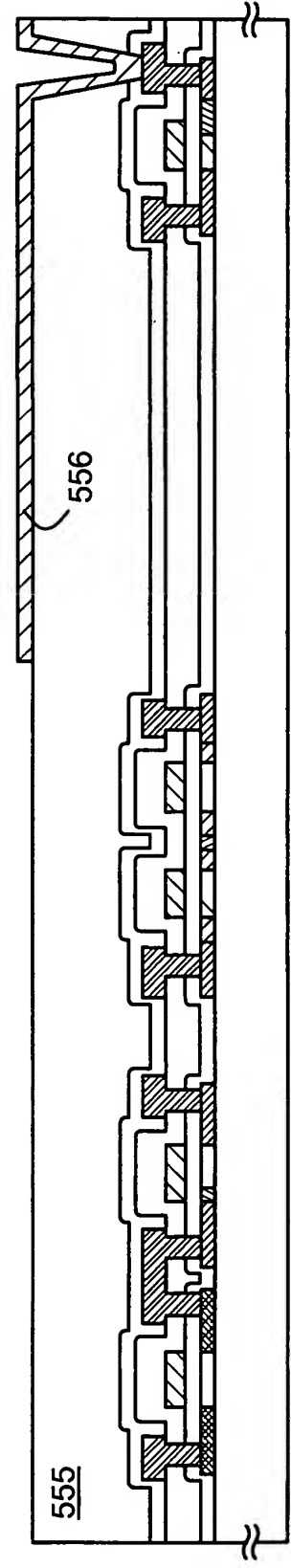


FIG. 14B

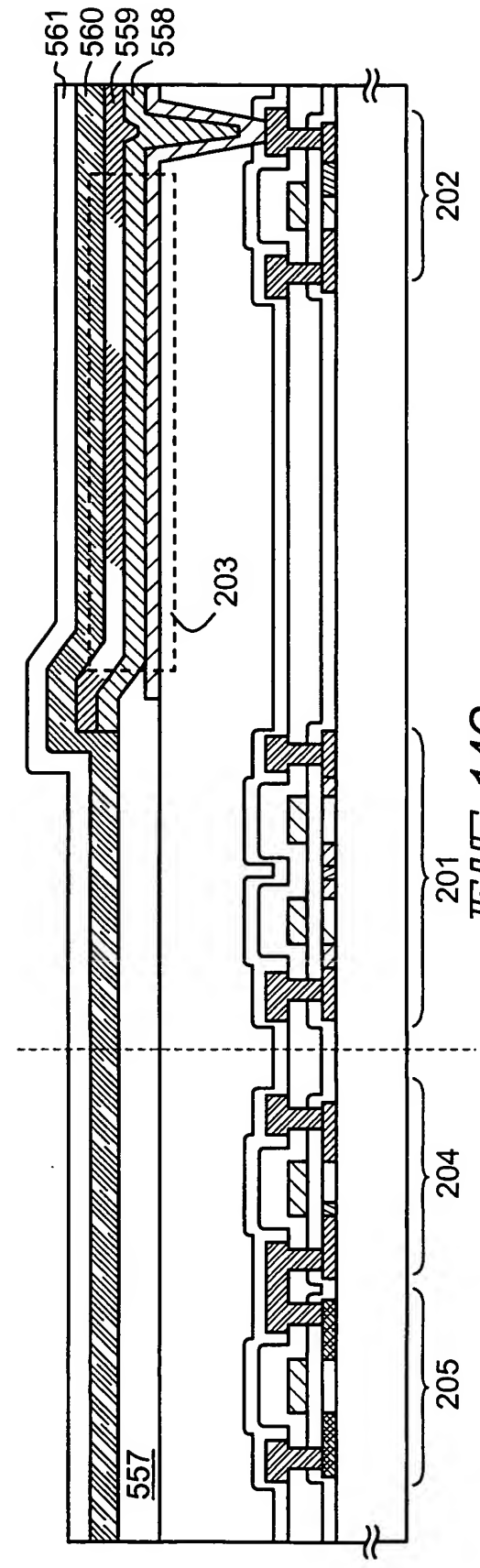


FIG. 14C



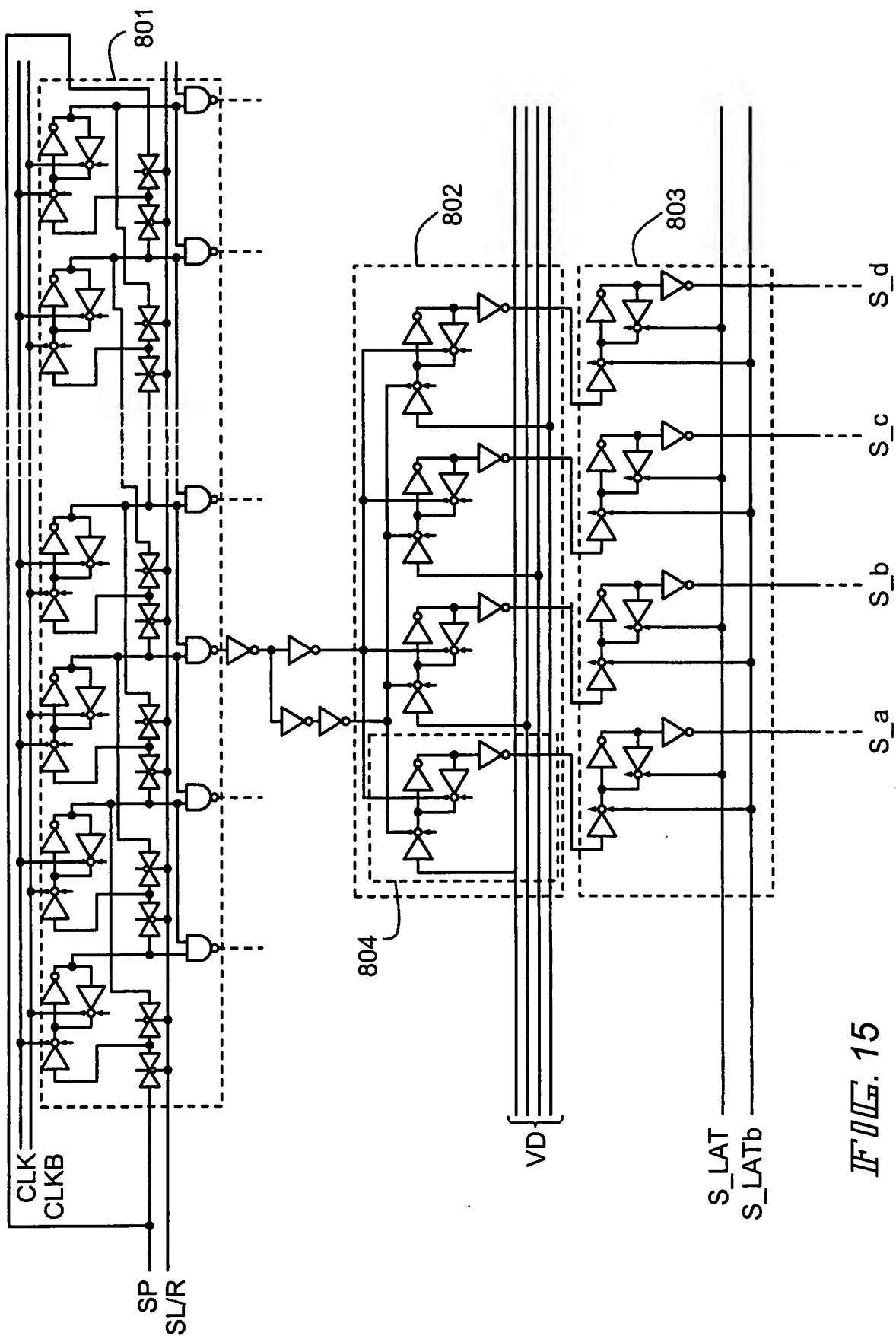


FIG. 15

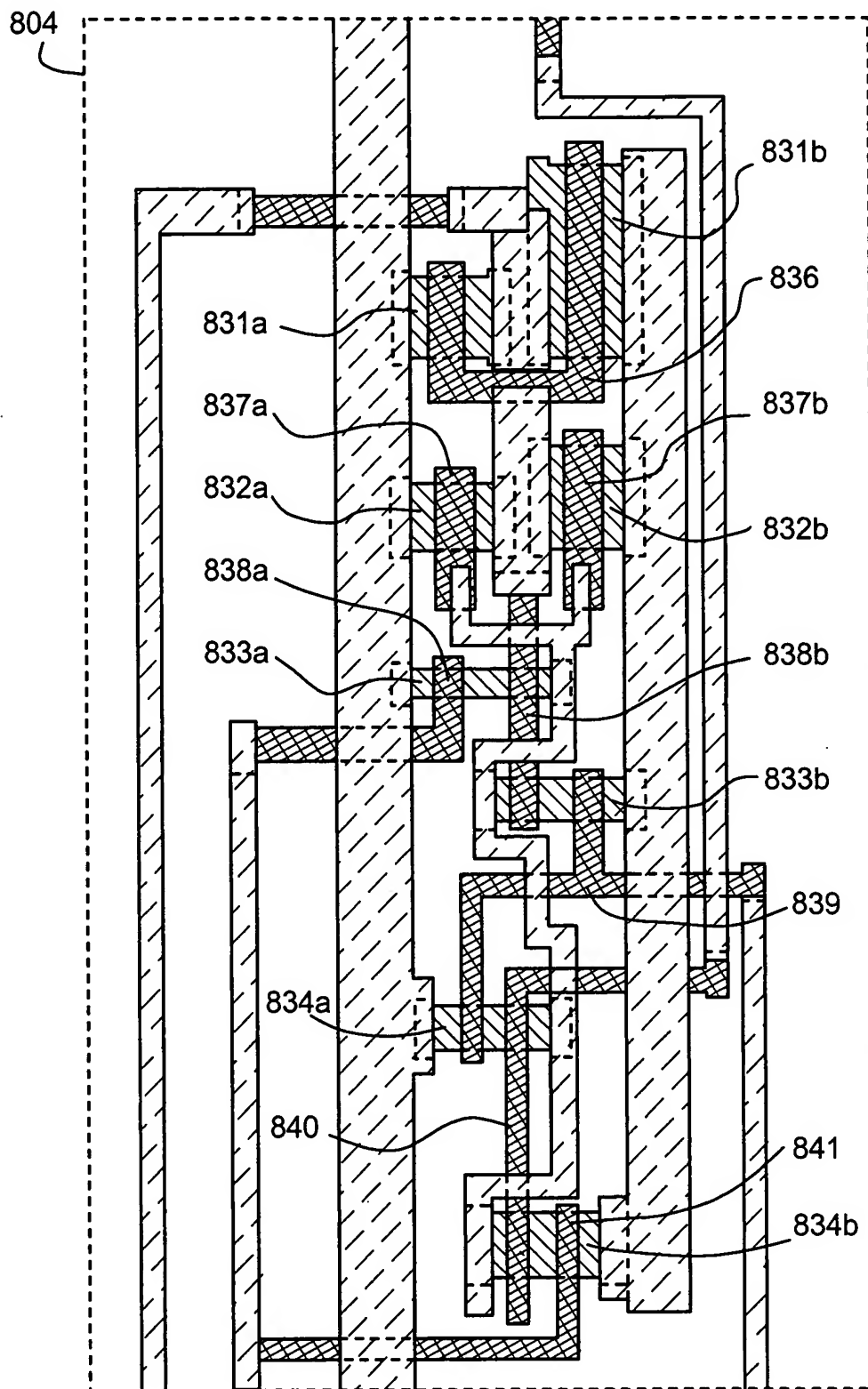
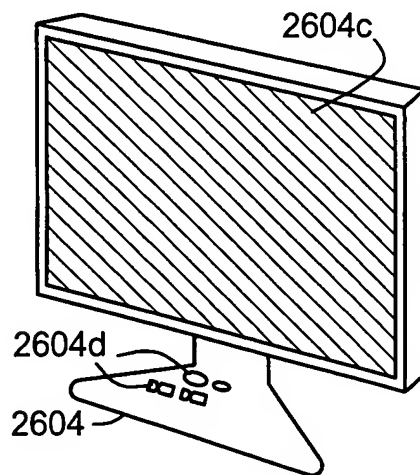
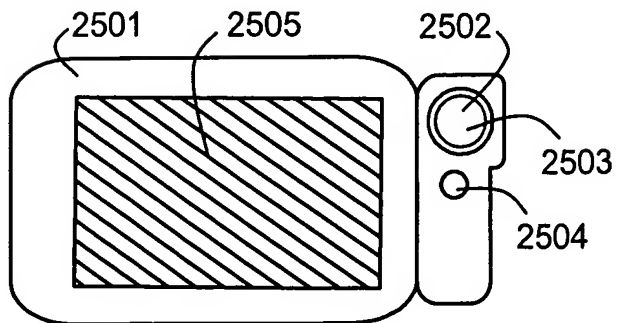
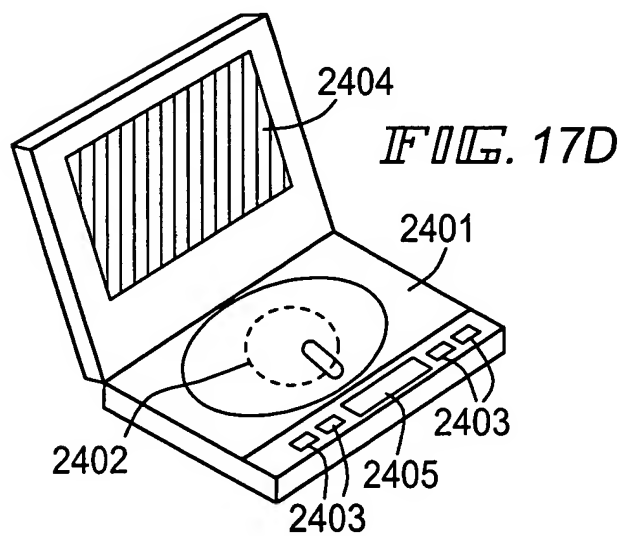
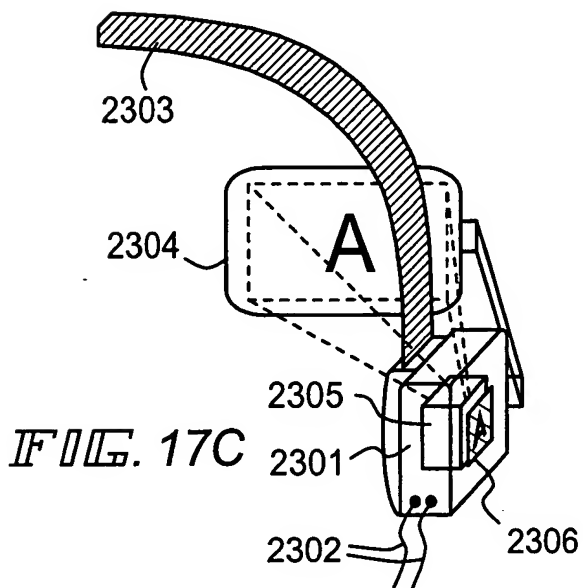
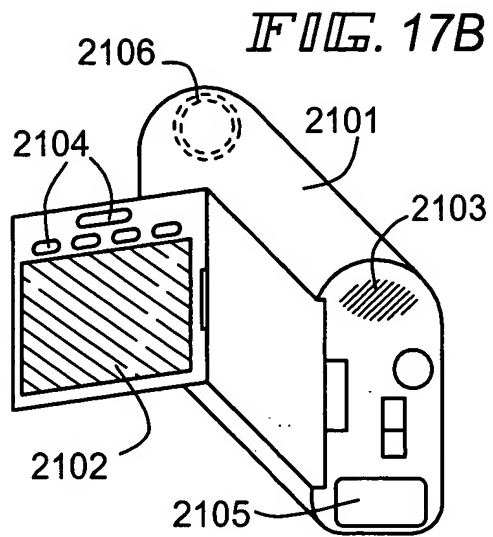
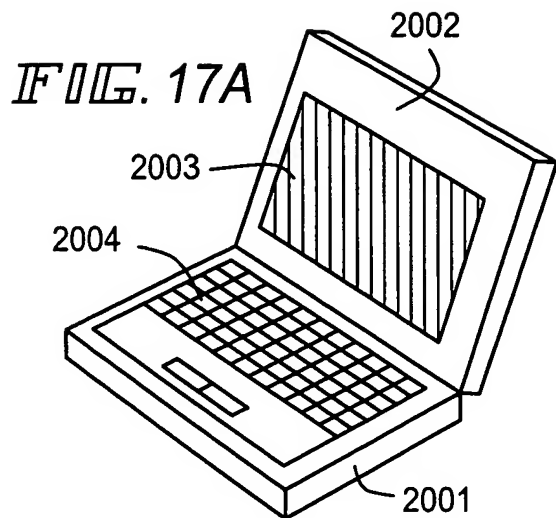


FIG. 16



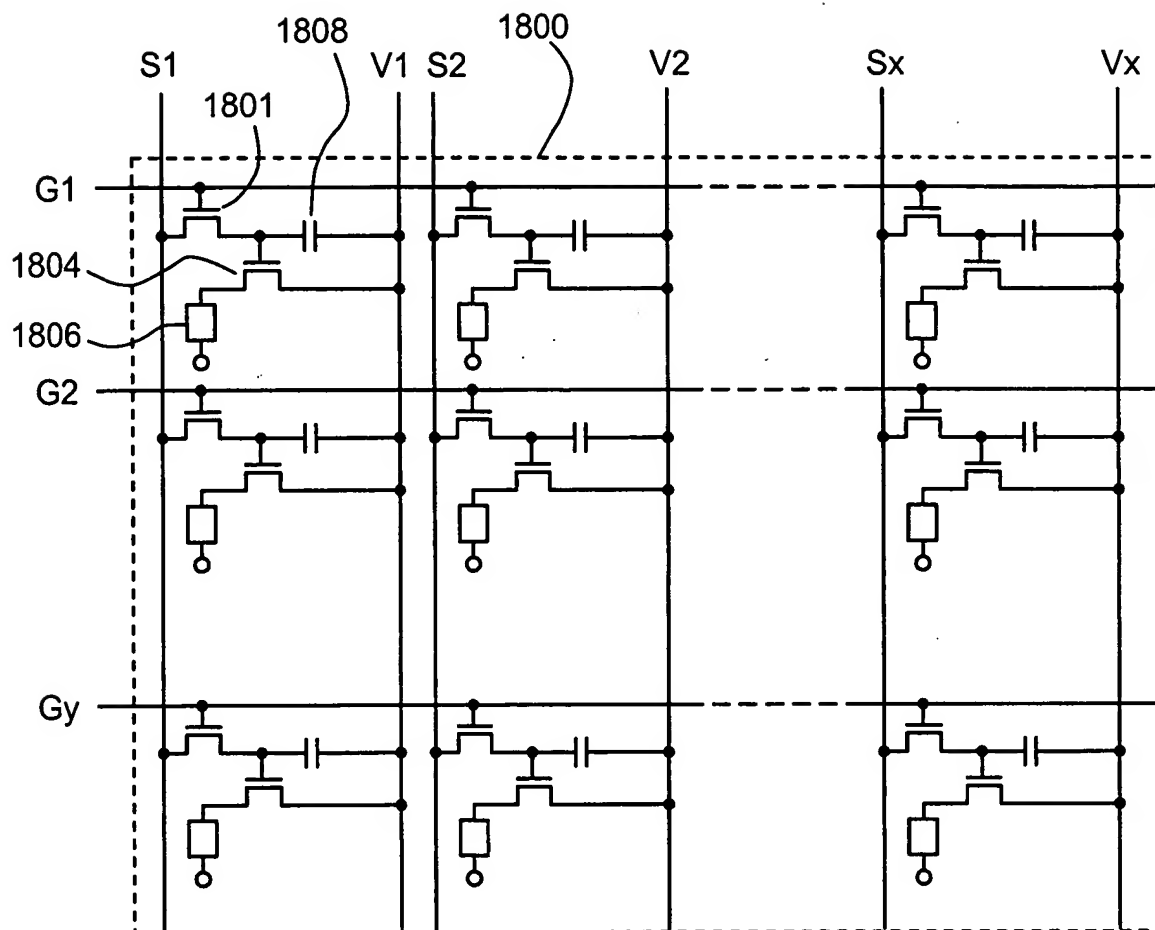


FIG. 18
PRIOR ART

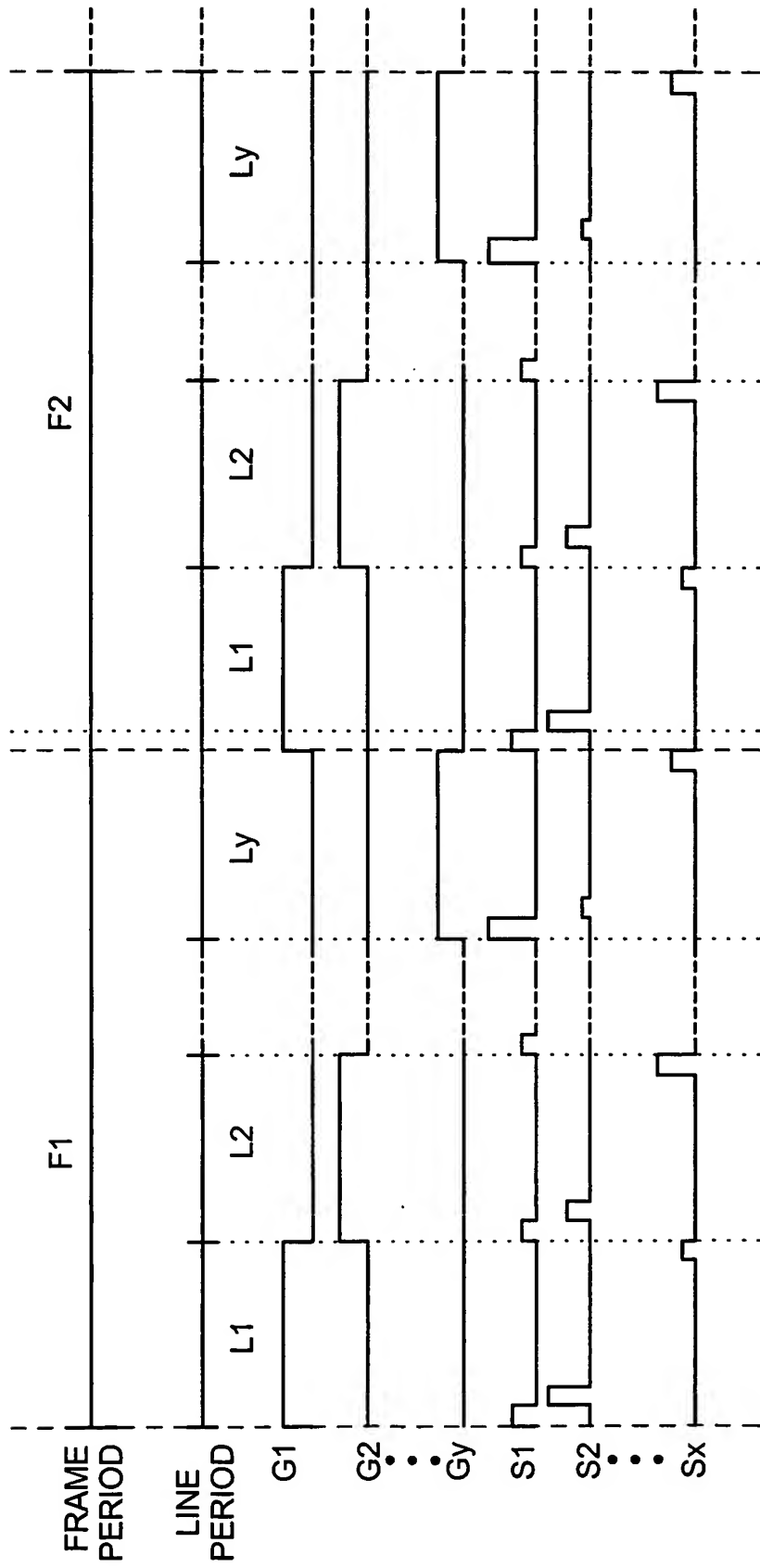


FIG. 19
PRIOR ART

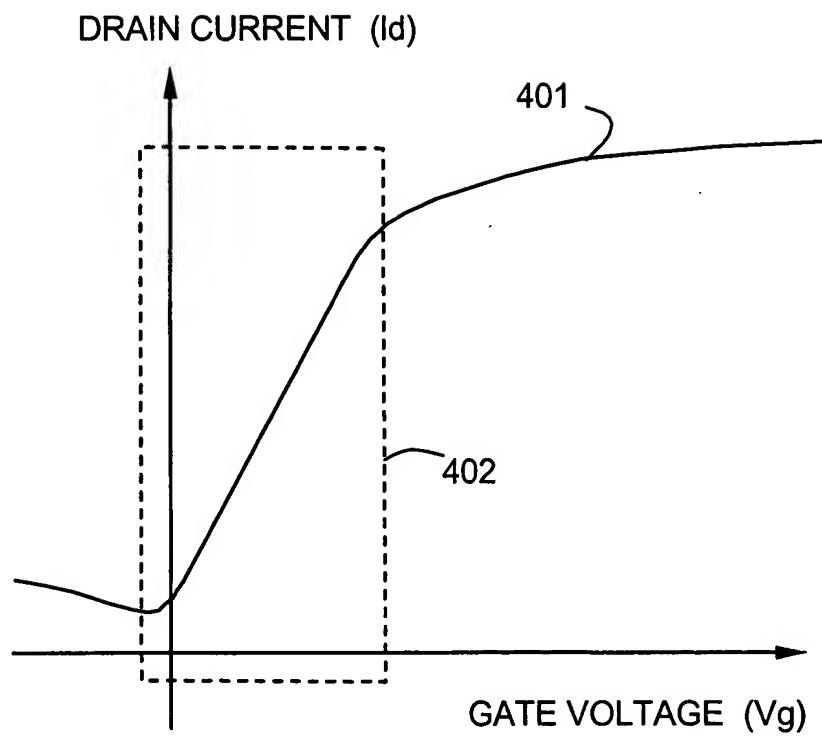


FIG. 20
PRIOR ART

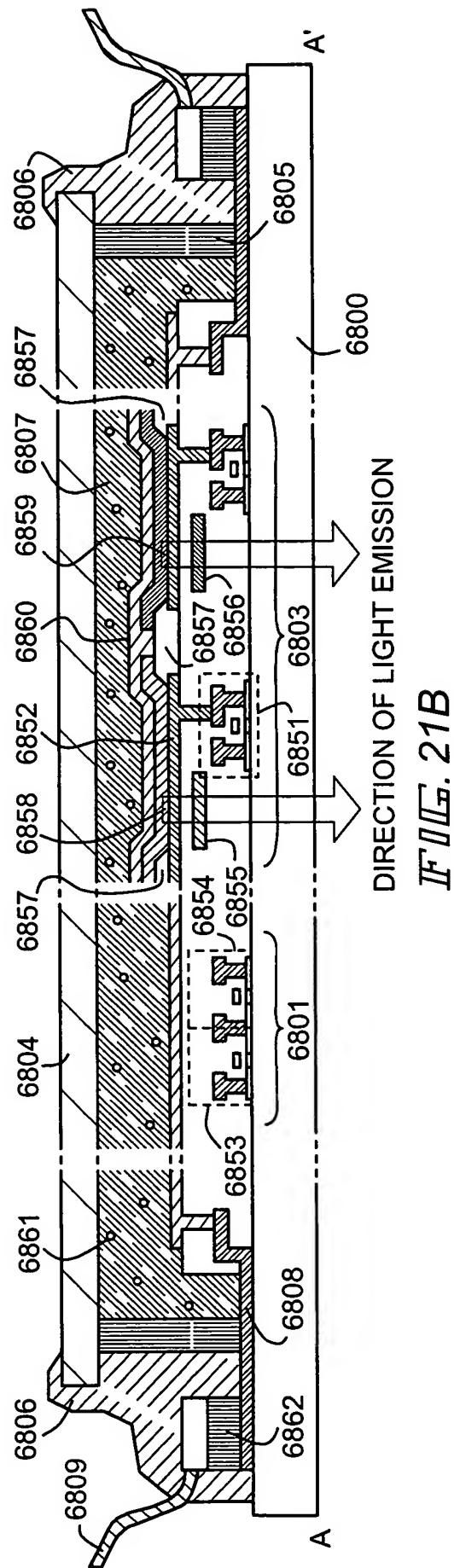
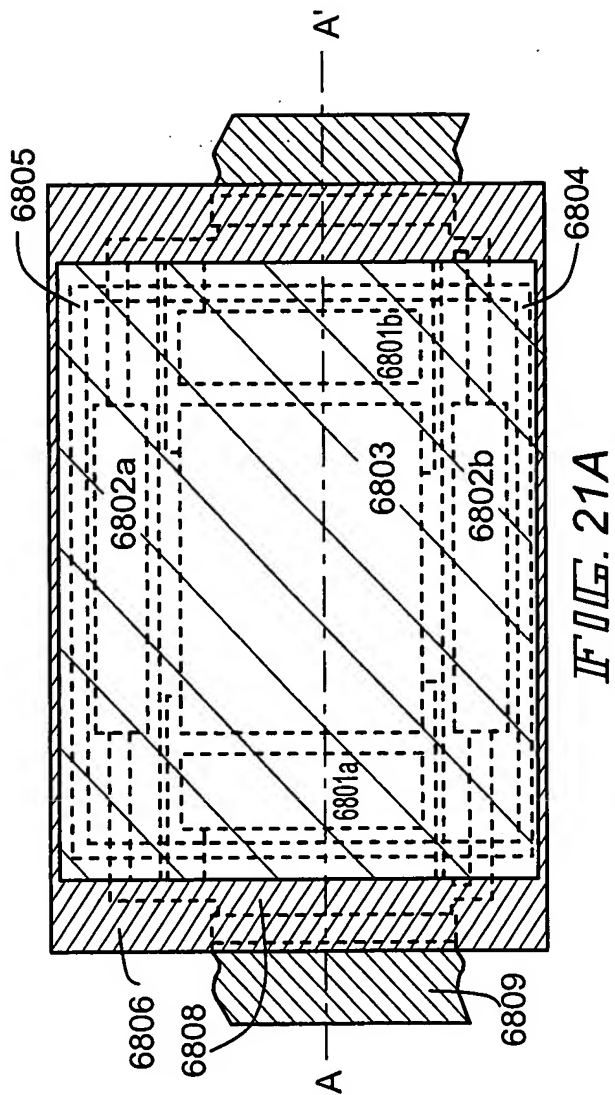




FIG. 22

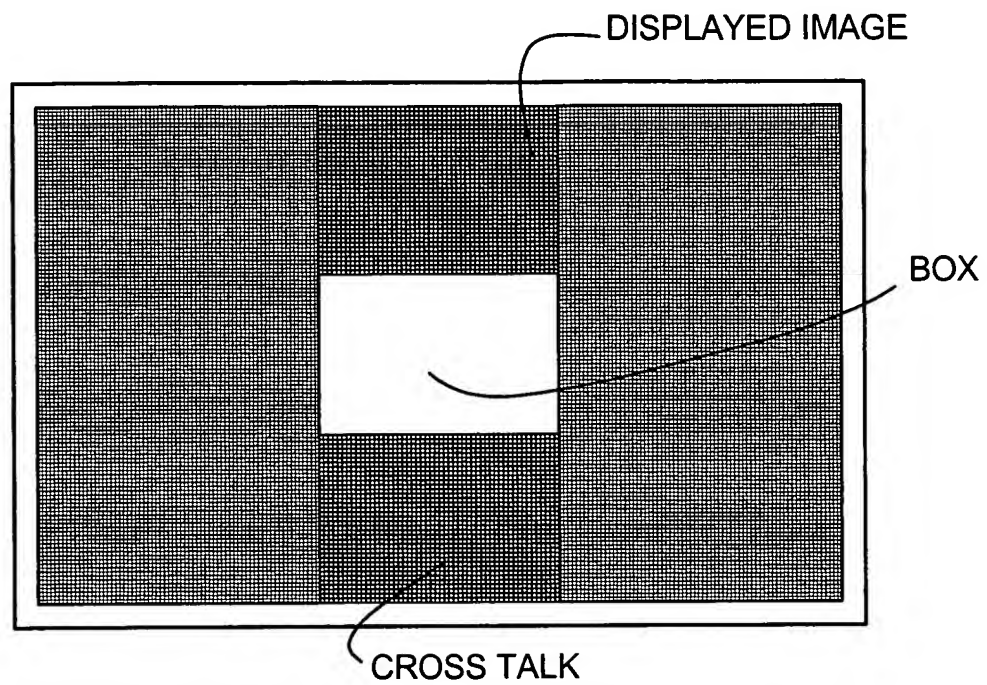


FIG. 23
PRIOR ART

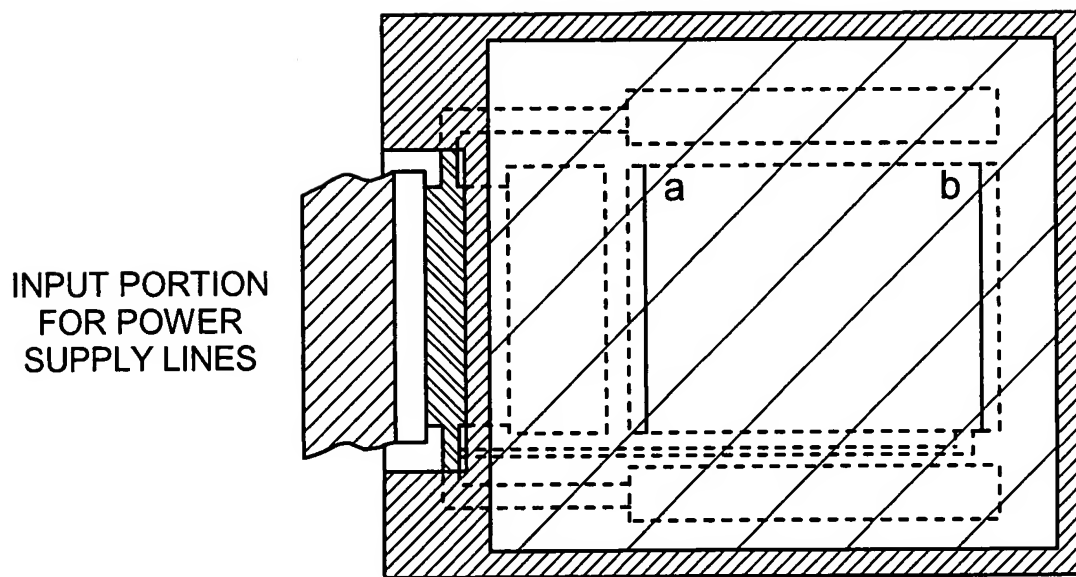


FIG. 24
PRIOR ART



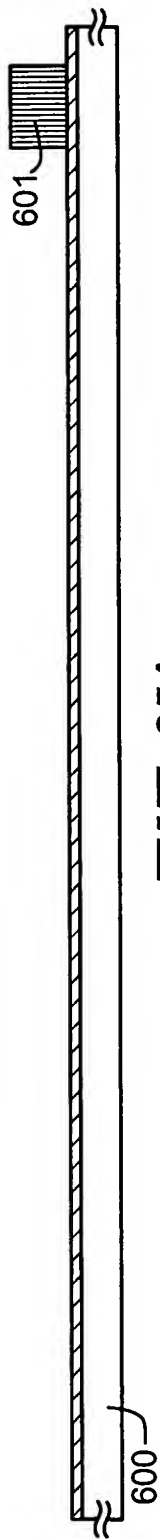


FIG. 25A



FIG. 25B



FIG. 25C

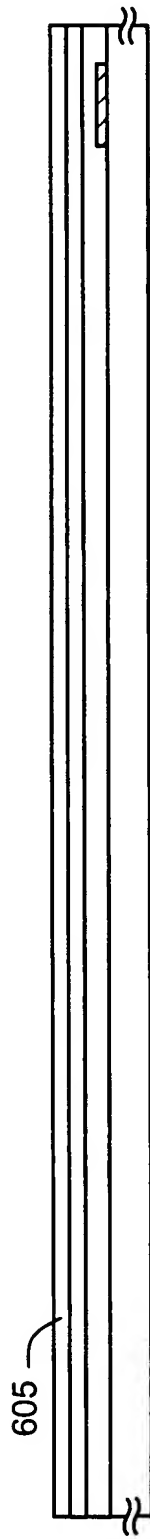


FIG. 25D

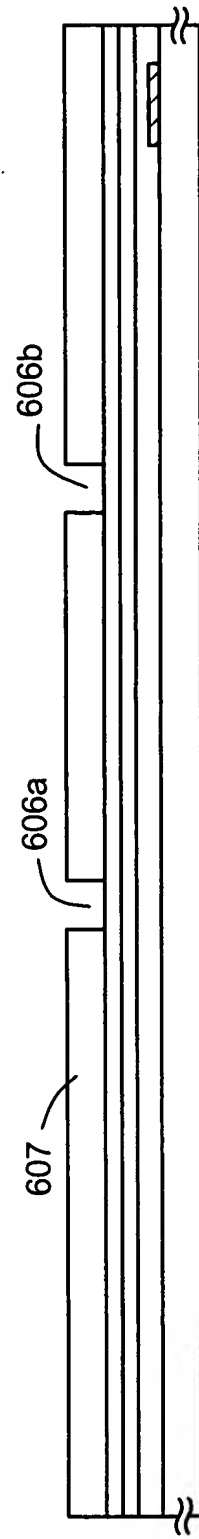


FIG. 25E



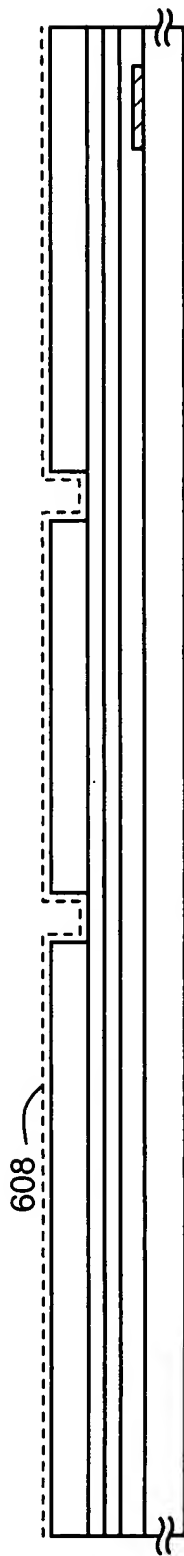


FIG. 26A

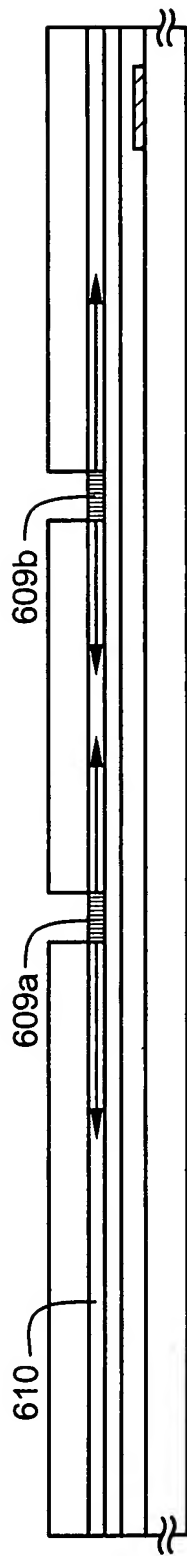


FIG. 26B

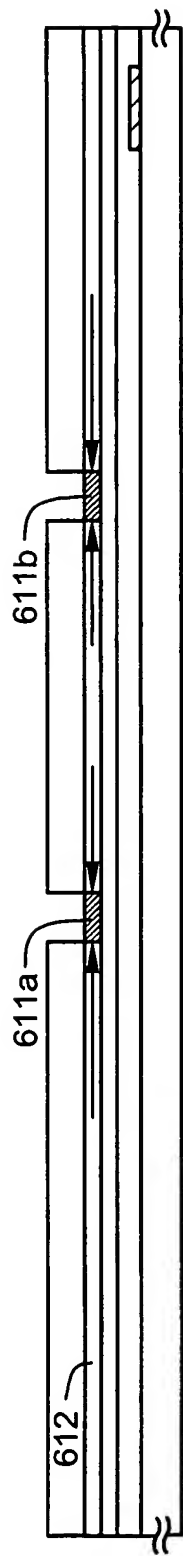


FIG. 26C

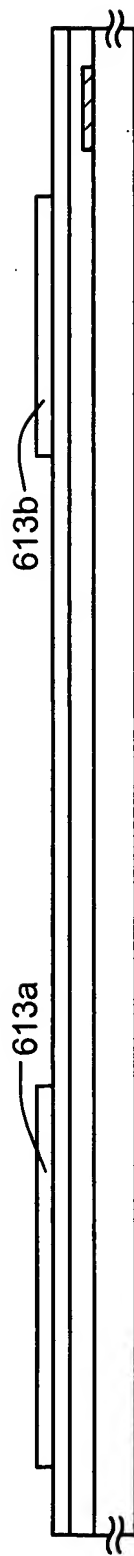


FIG. 26D

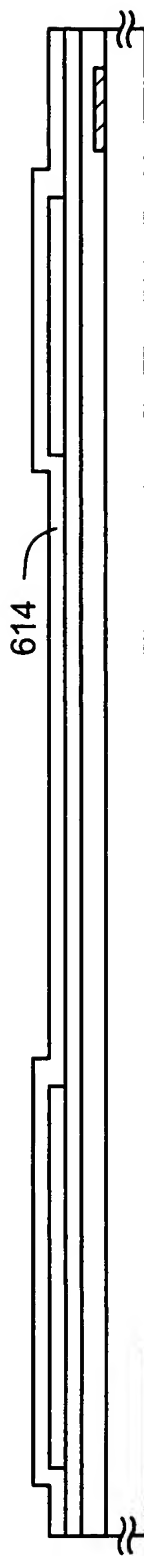


FIG. 26E

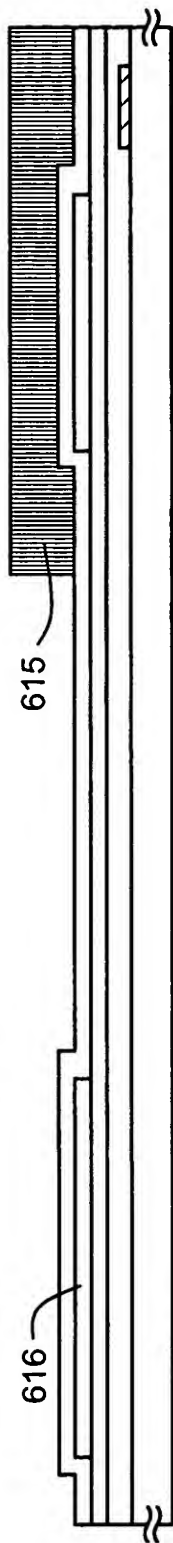


FIG. 27A

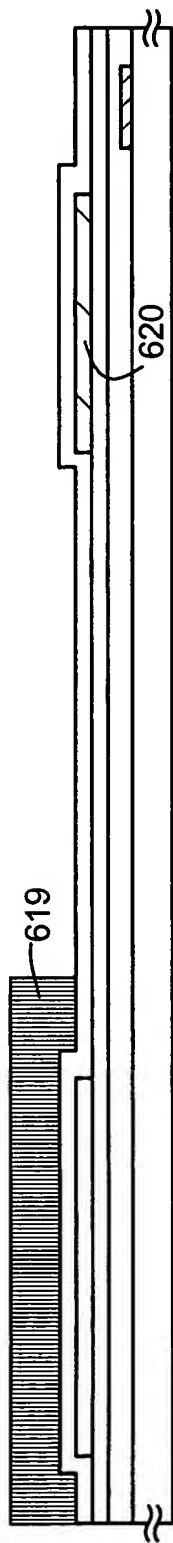


FIG. 27B

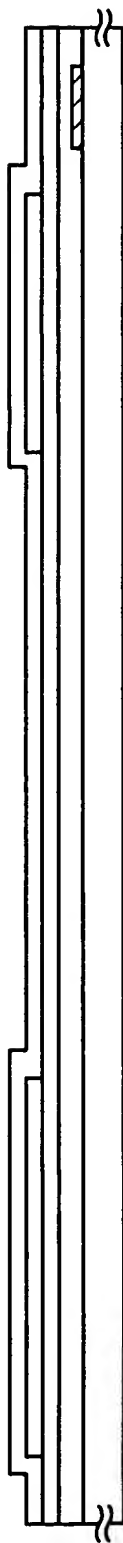


FIG. 27C

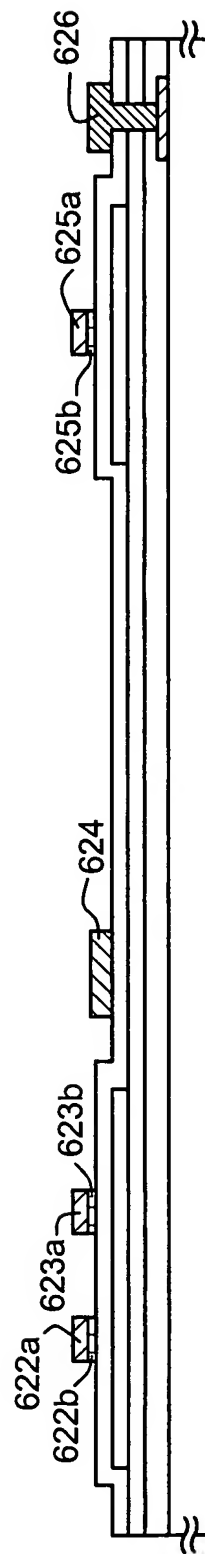


FIG. 27D





FIG. 28A

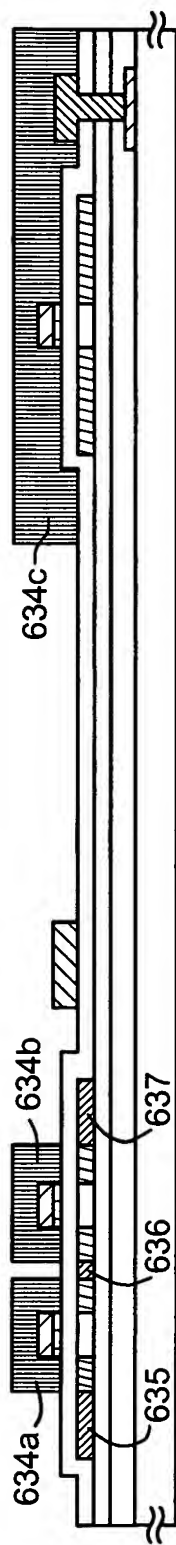


FIG. 28B

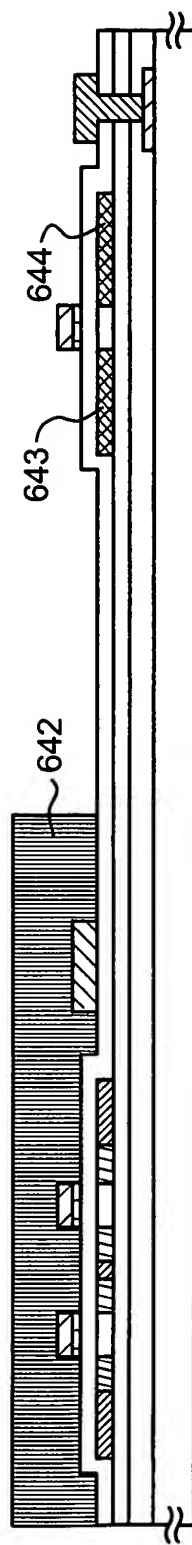


FIG. 28C

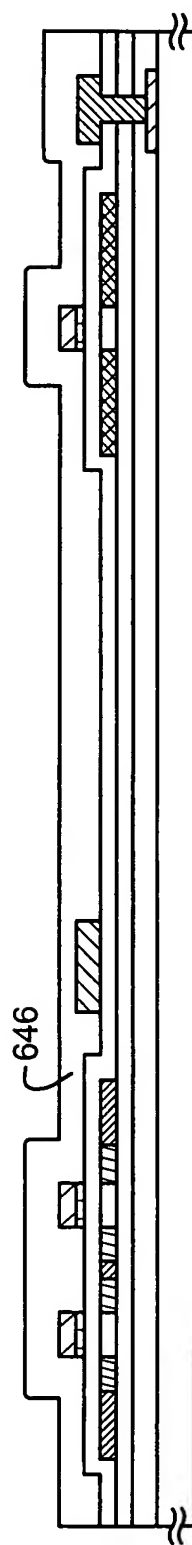


FIG. 28D

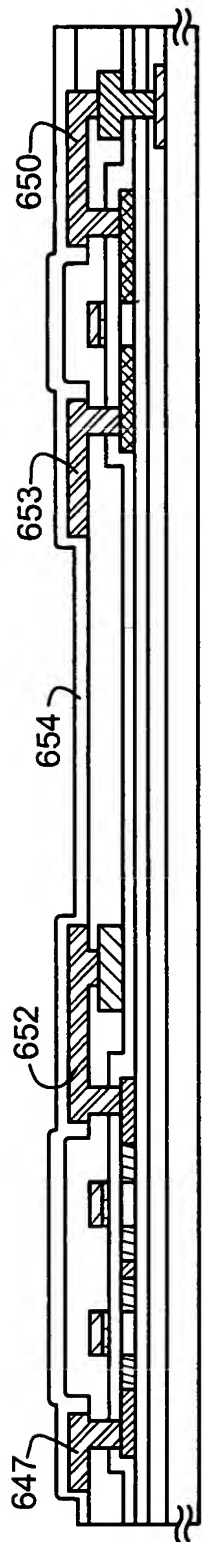


FIG. 29A

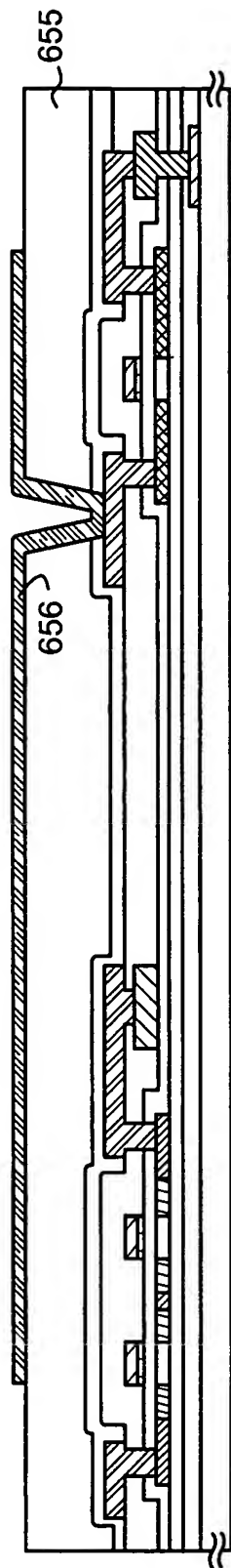


FIG. 29B

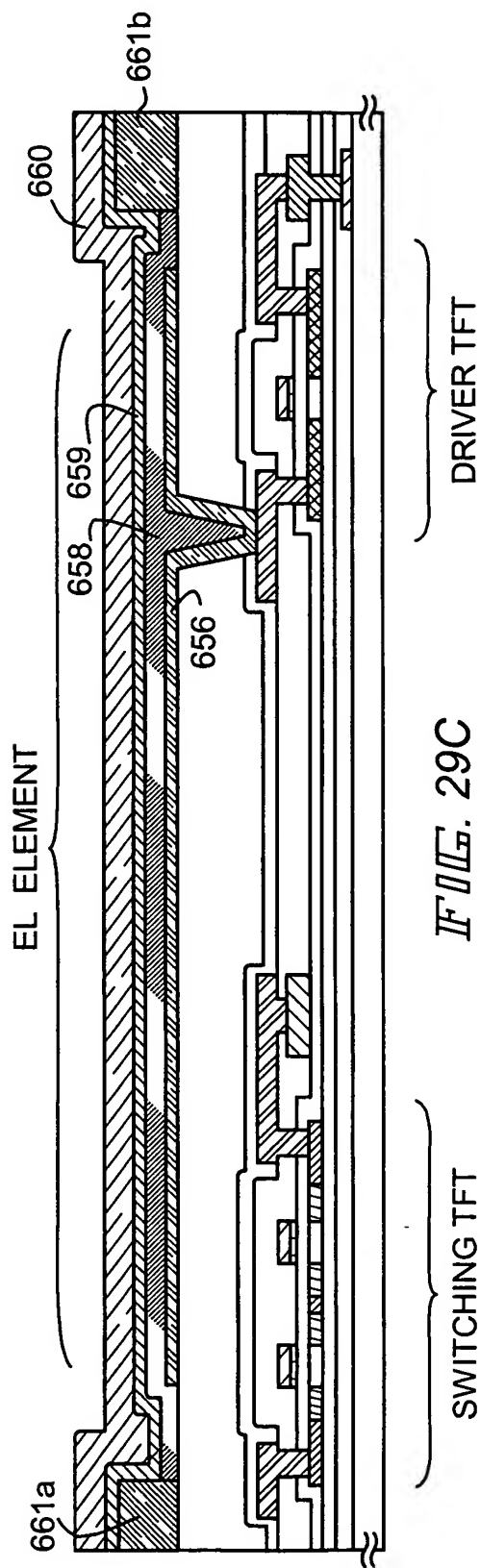


FIG. 29C

SWITCHING TFT

DRIVER TFT

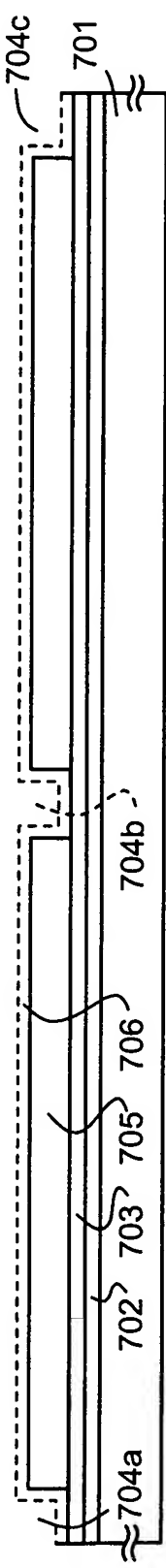


FIG. 30A

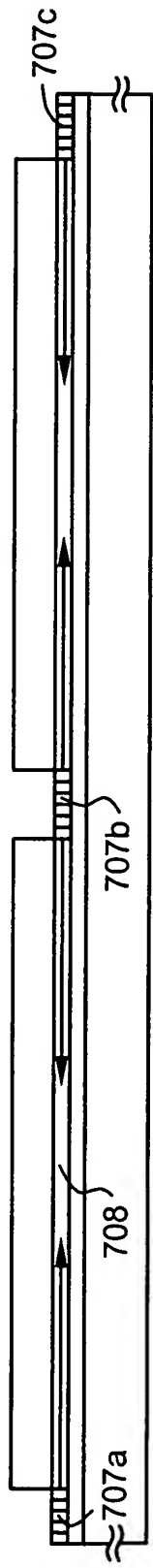


FIG. 30B

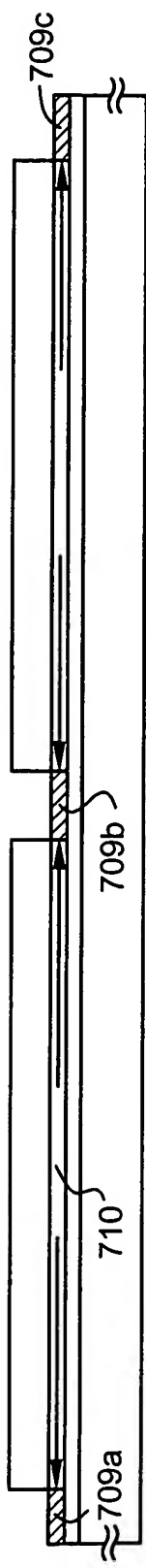


FIG. 30C

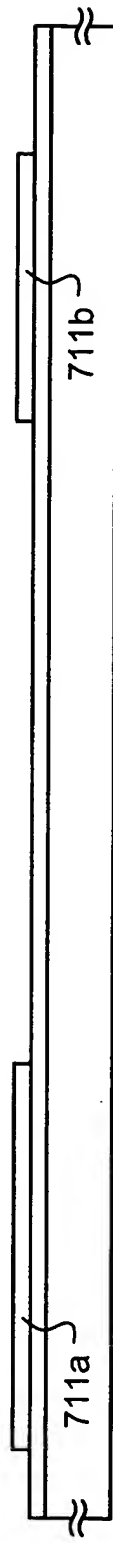


FIG. 30D

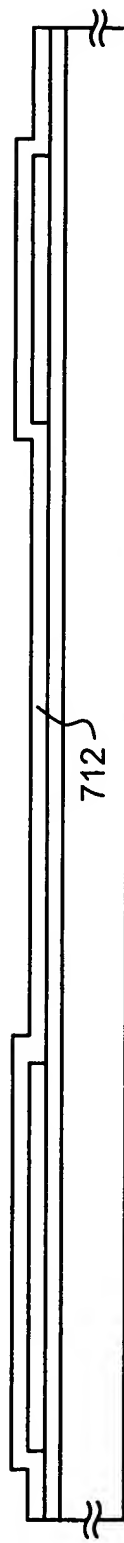


FIG. 30E

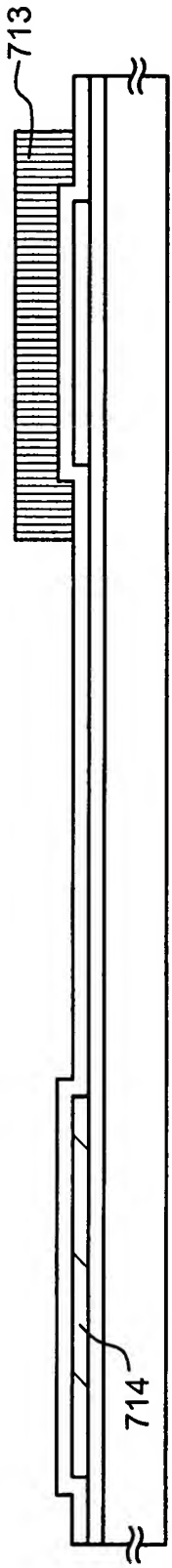


FIG. 31A

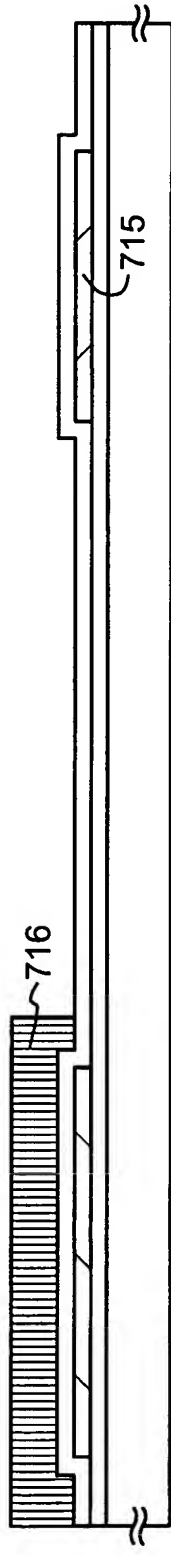


FIG. 31B



FIG. 31C

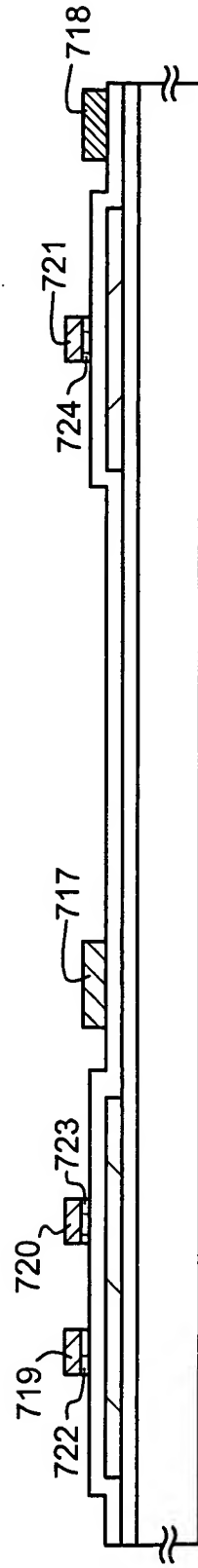


FIG. 31D



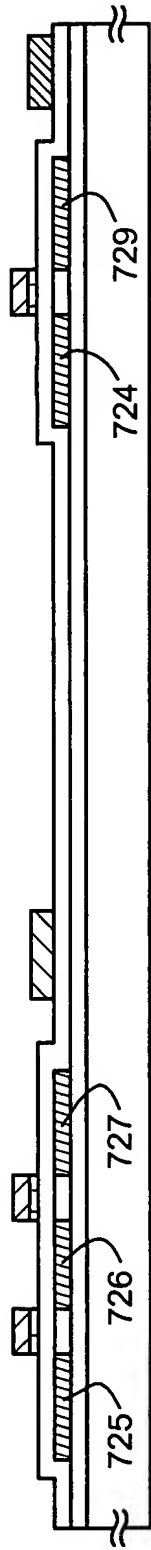


FIG. 32A

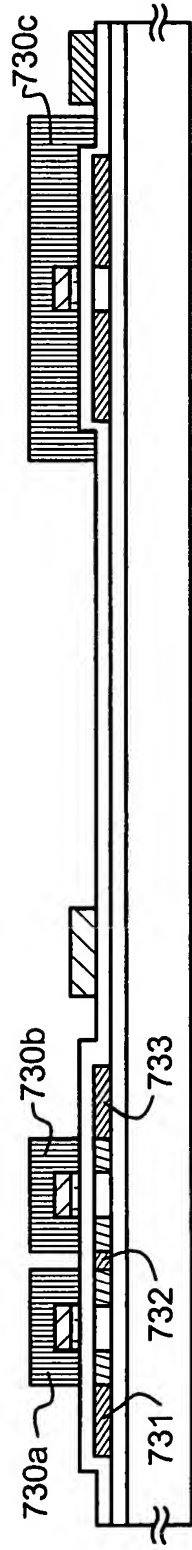


FIG. 32b

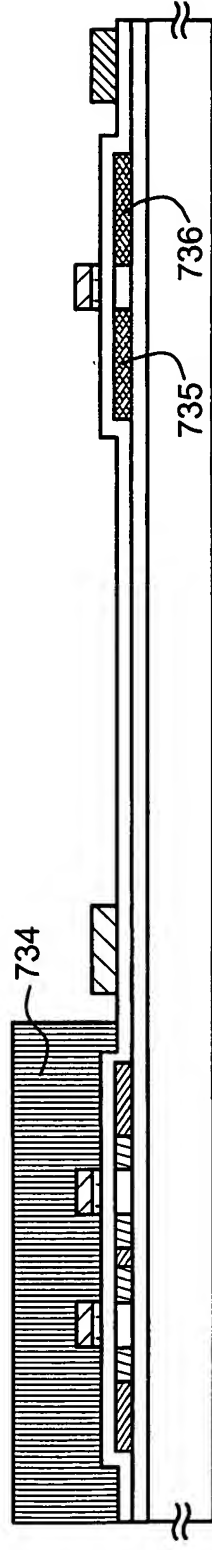


FIG. 32C

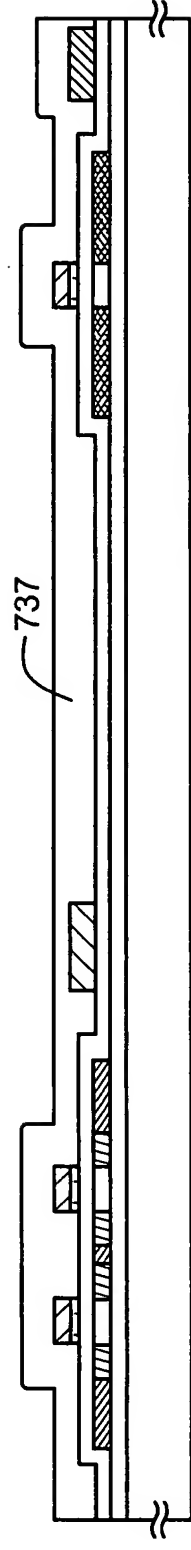


FIG. 32D

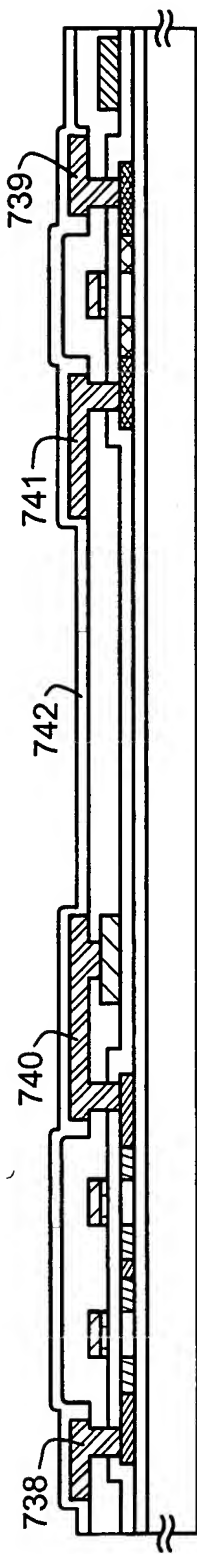


FIG. 33A

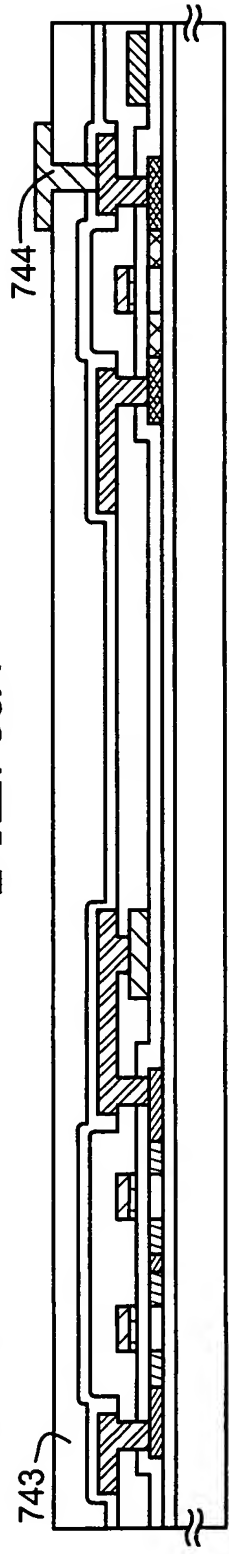


FIG. 33B

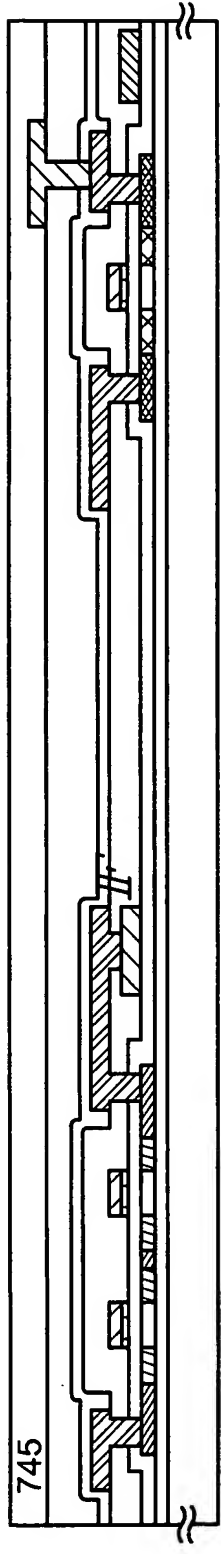


FIG. 33C

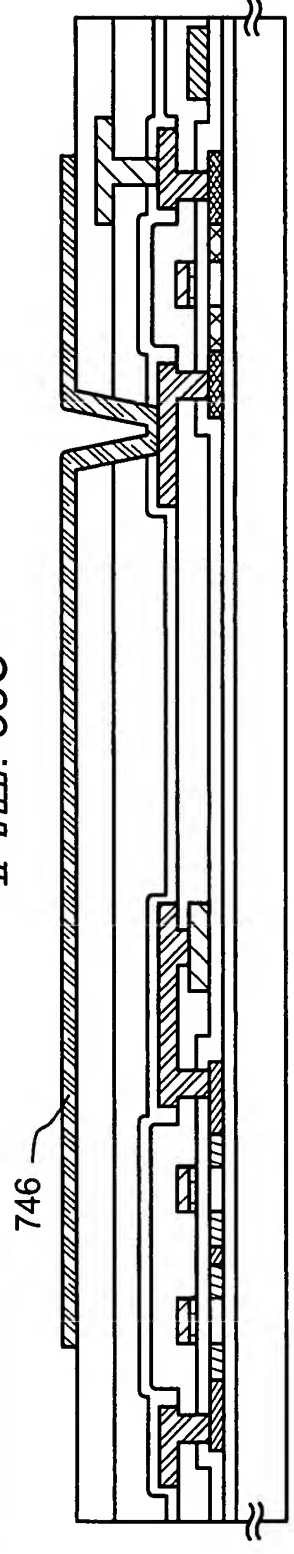


FIG. 33D

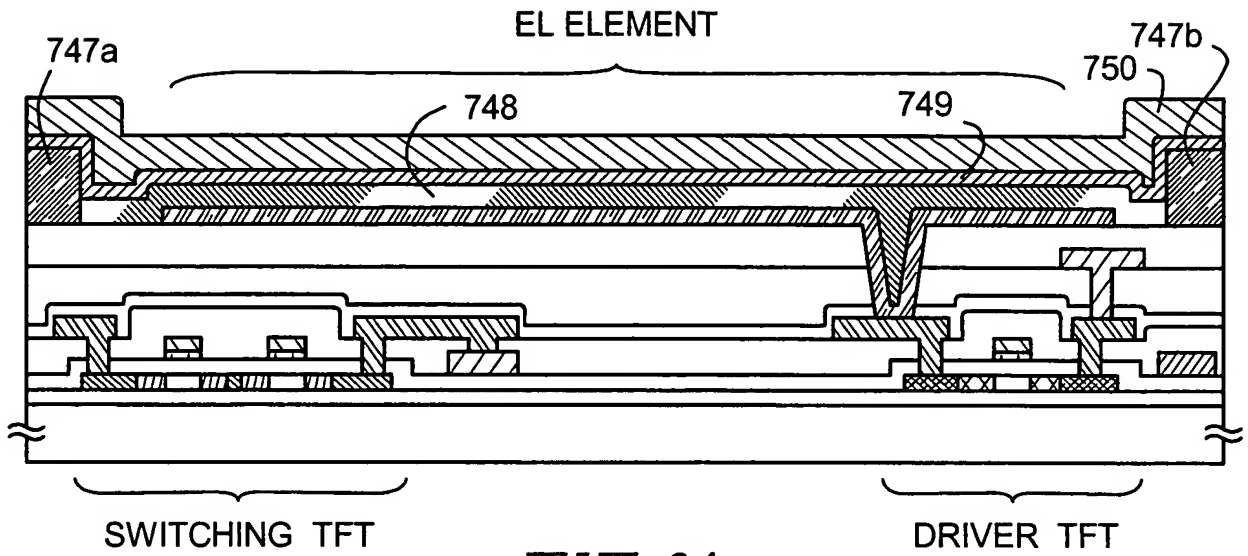


FIG. 34

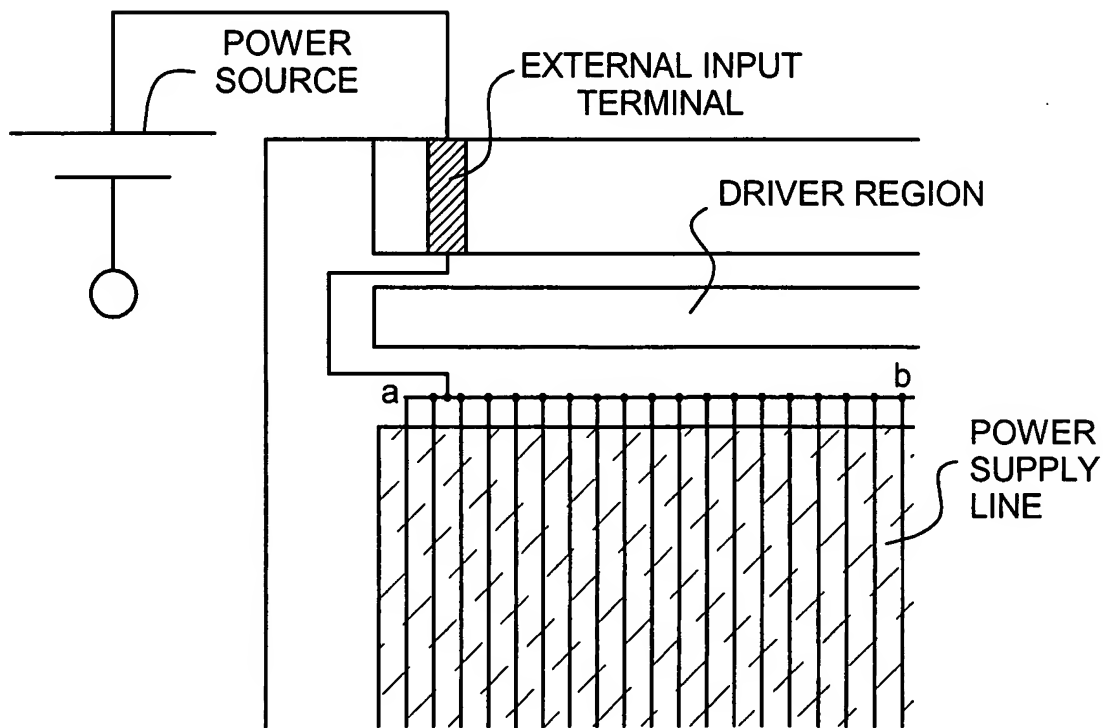


FIG. 35

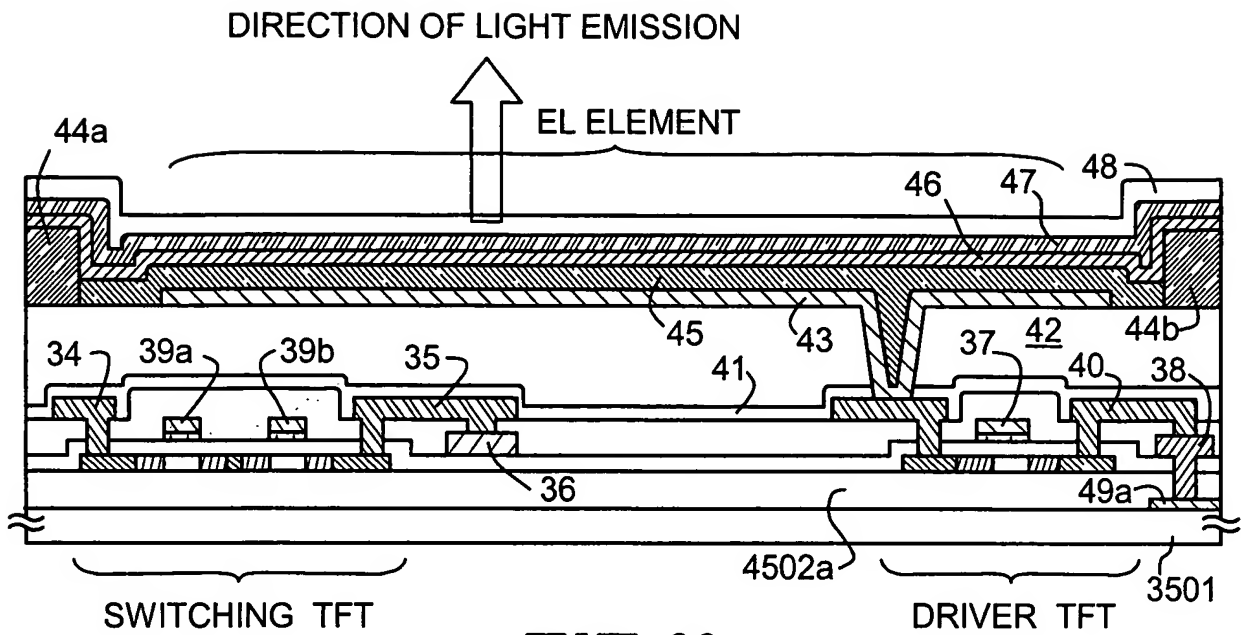


FIG. 36

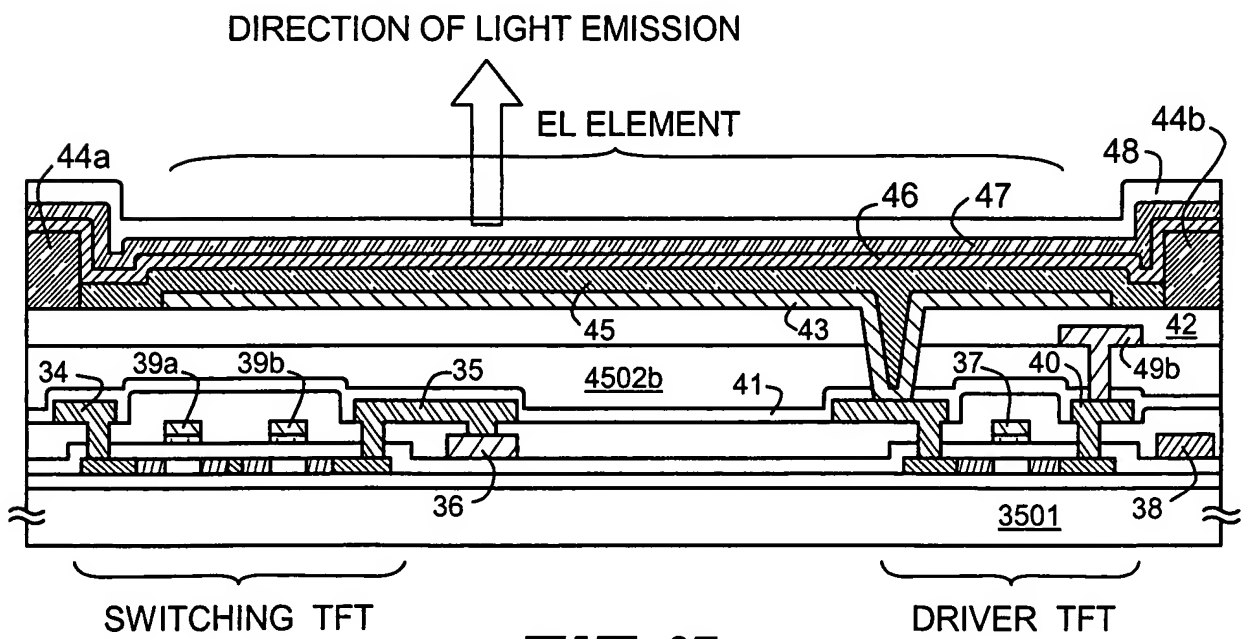


FIG. 37

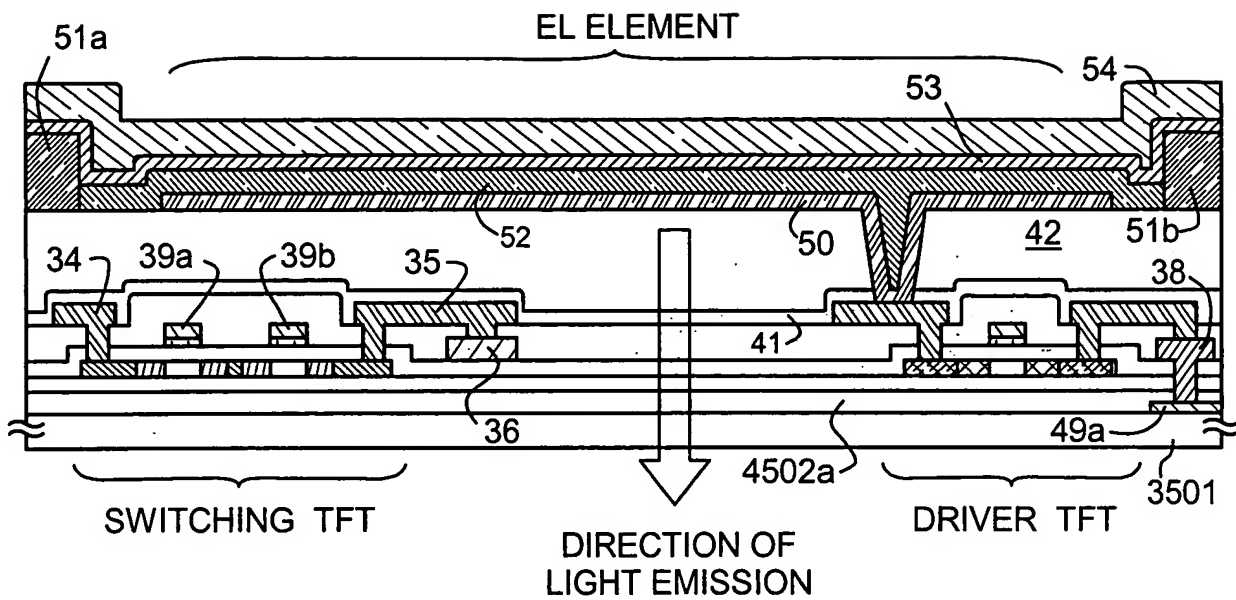


FIG. 38

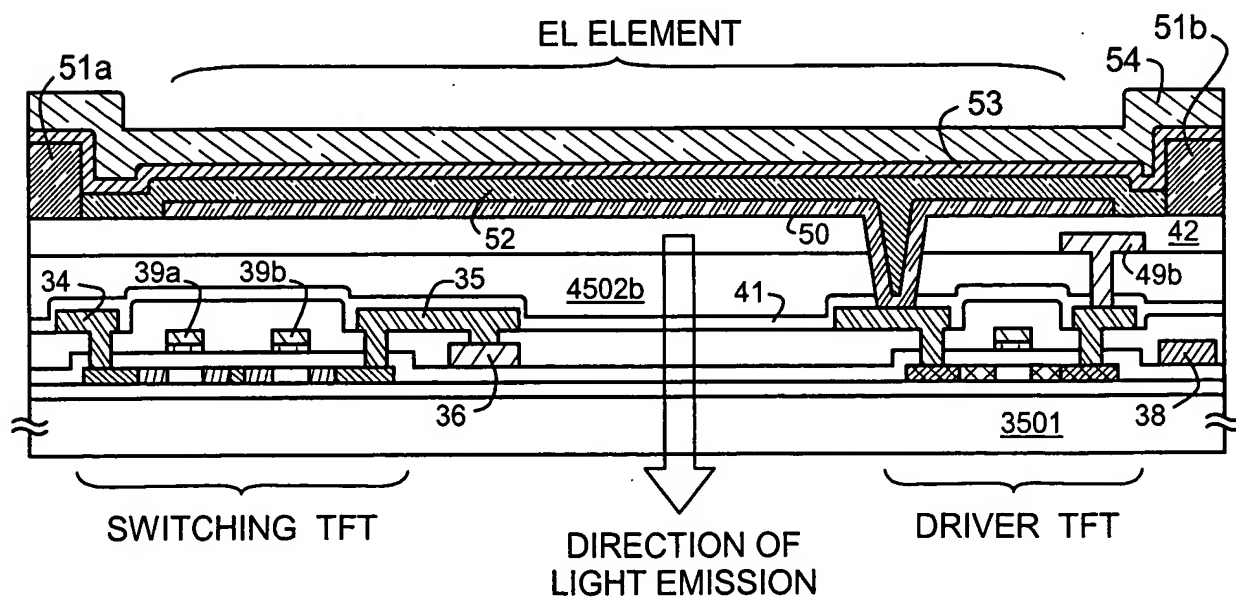


FIG. 39

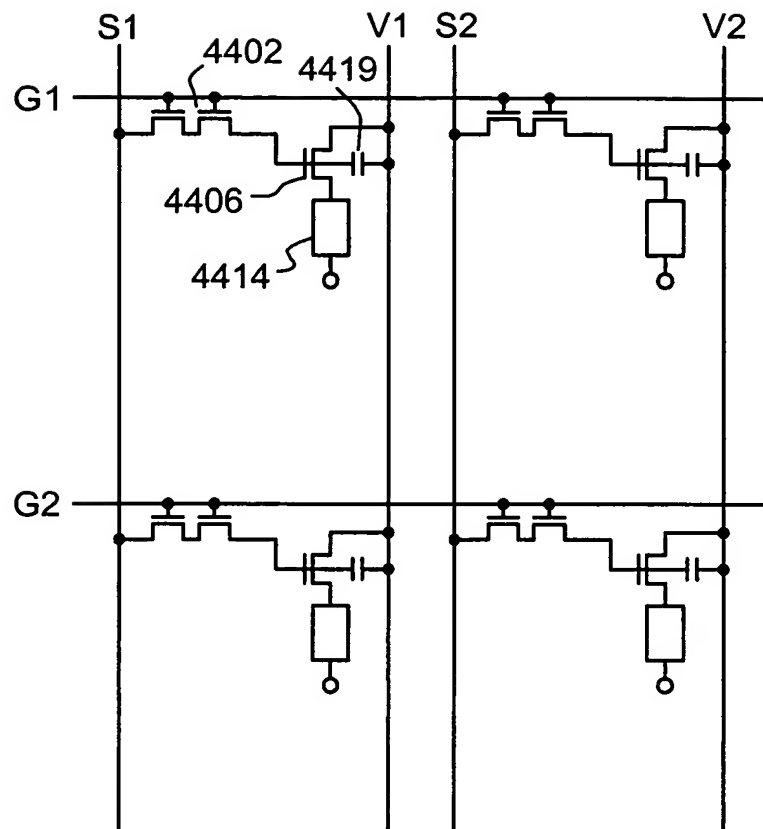


FIG. 40
PRIOR ART

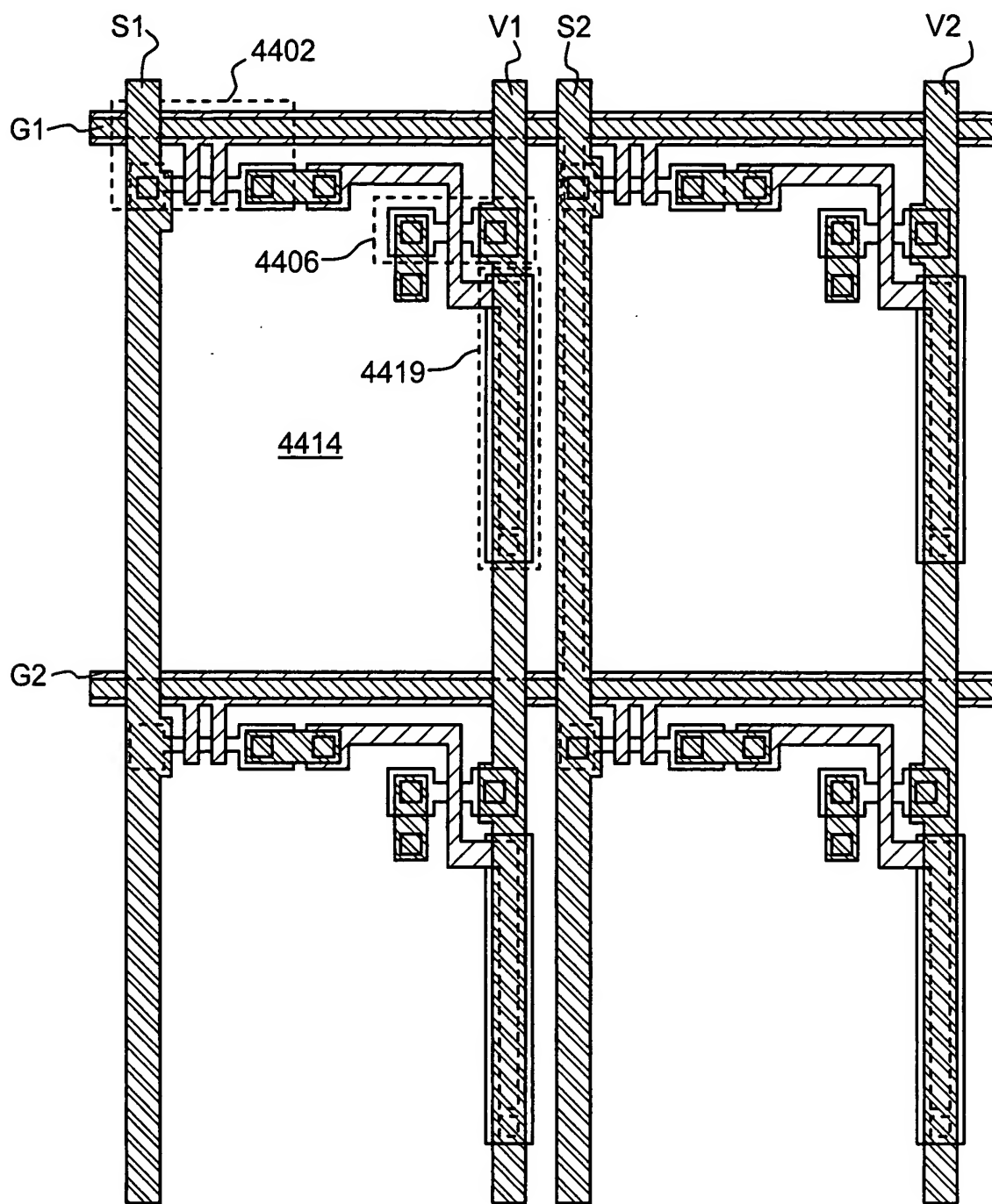


FIG. 41
PRIOR ART

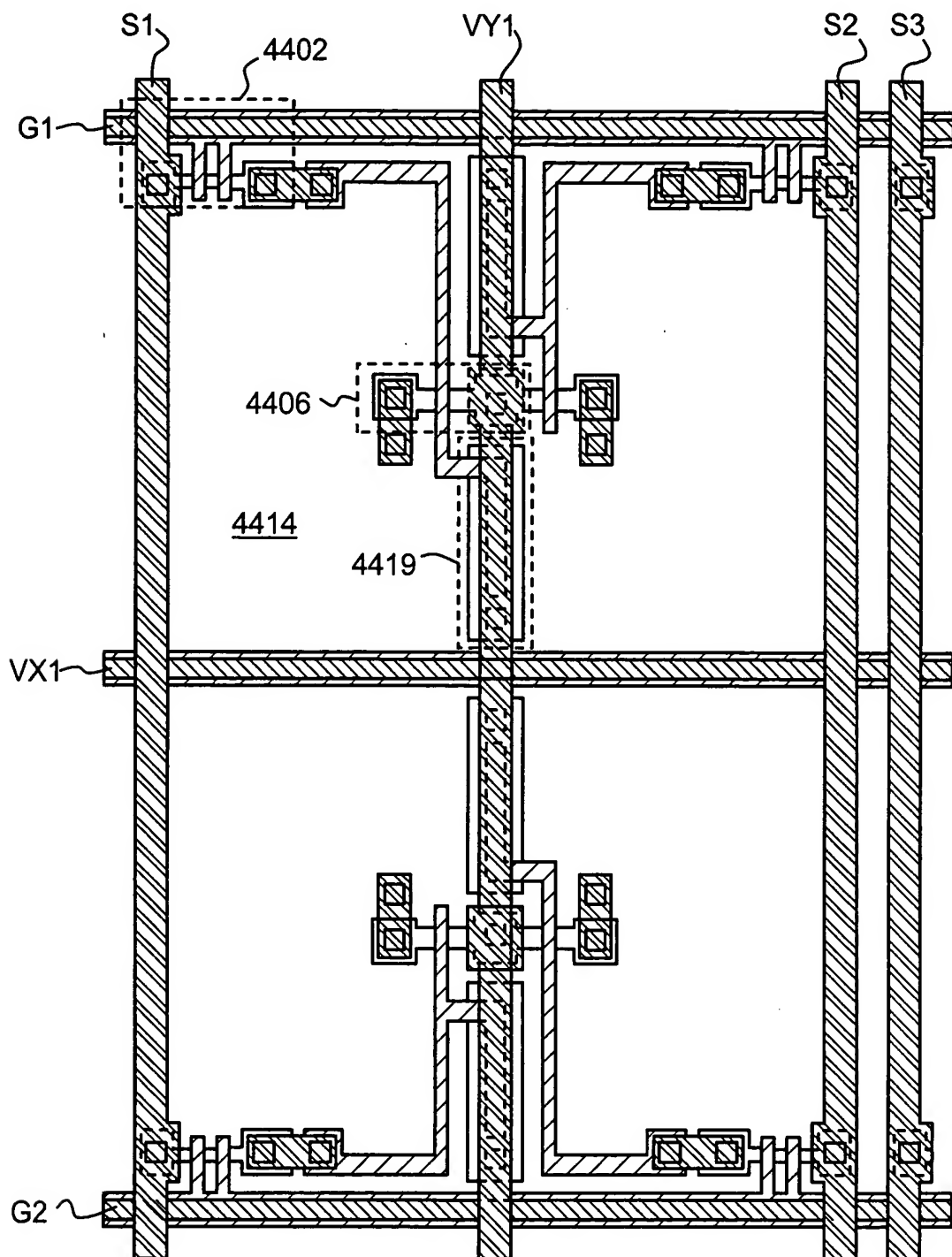


FIG. 42

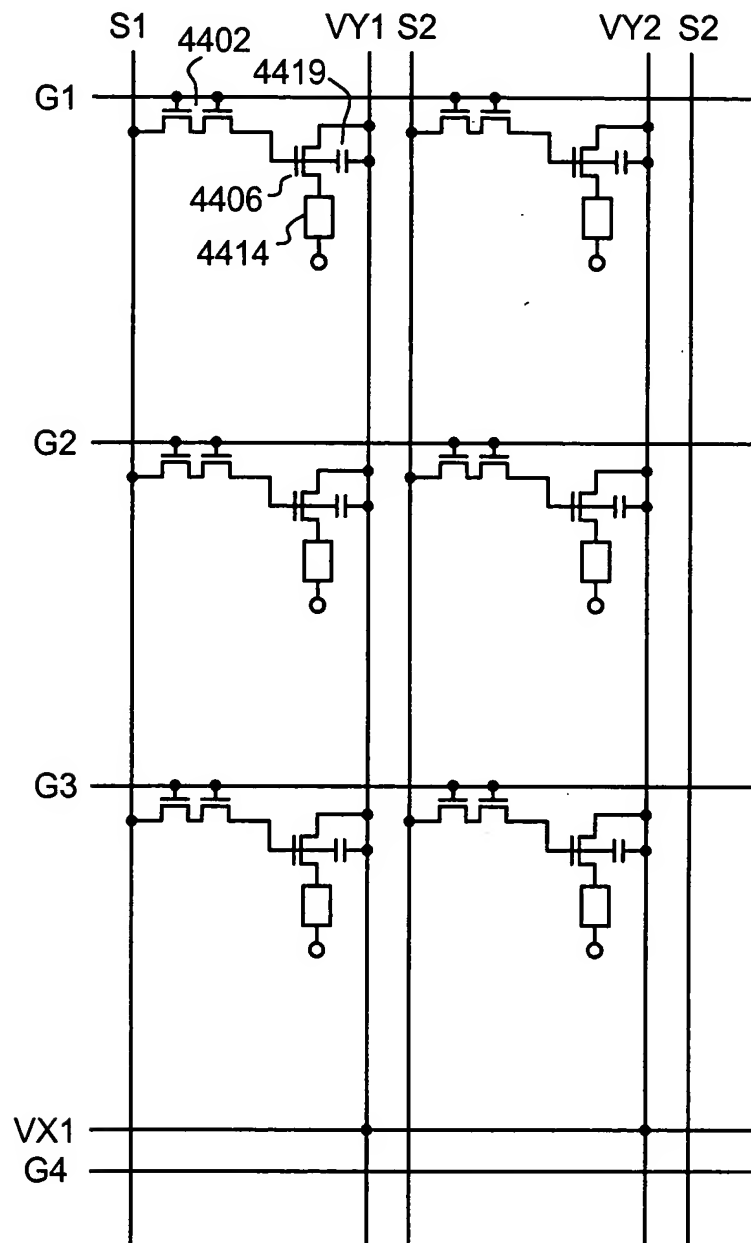


FIG. 43

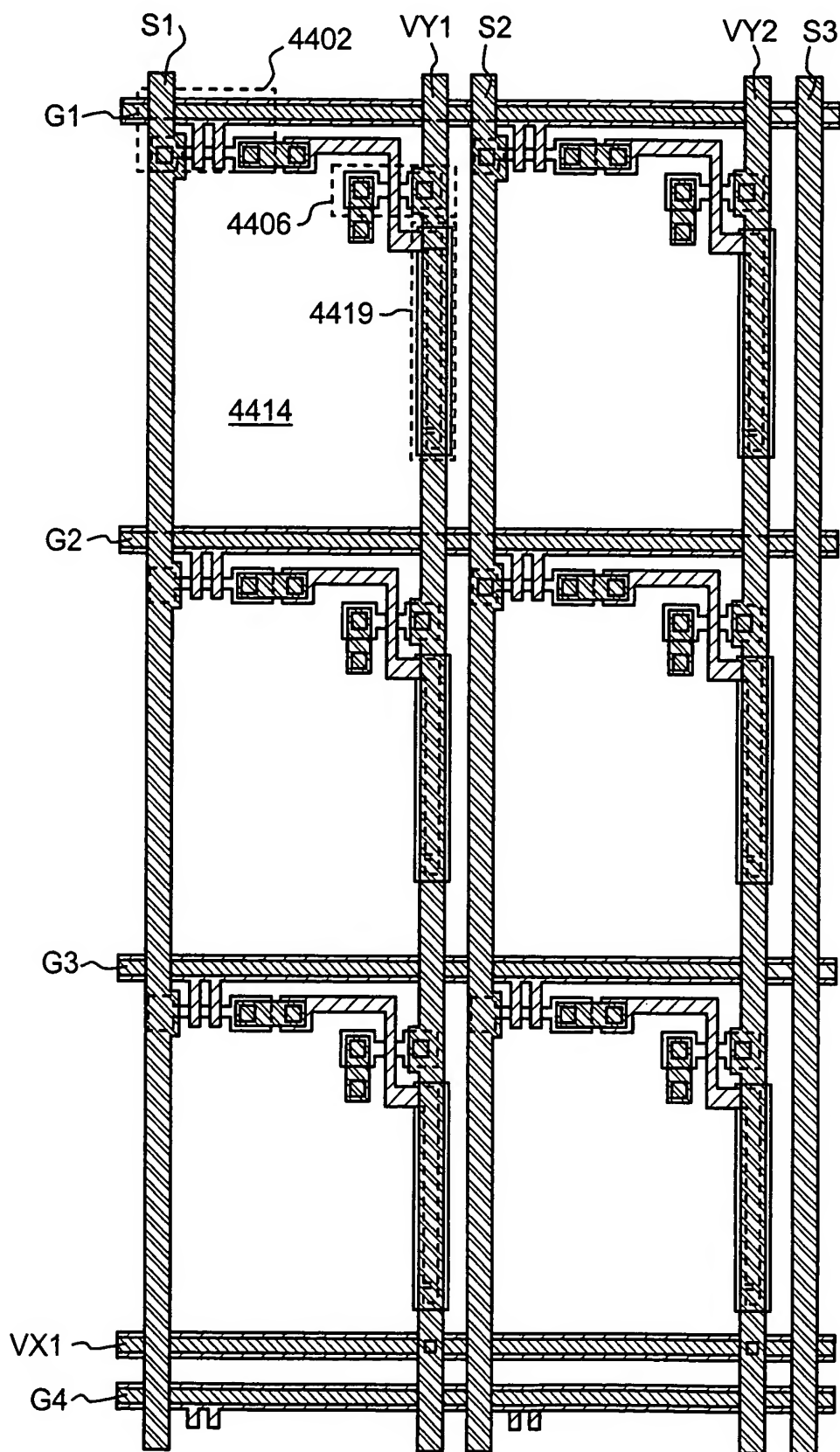


FIG. 44

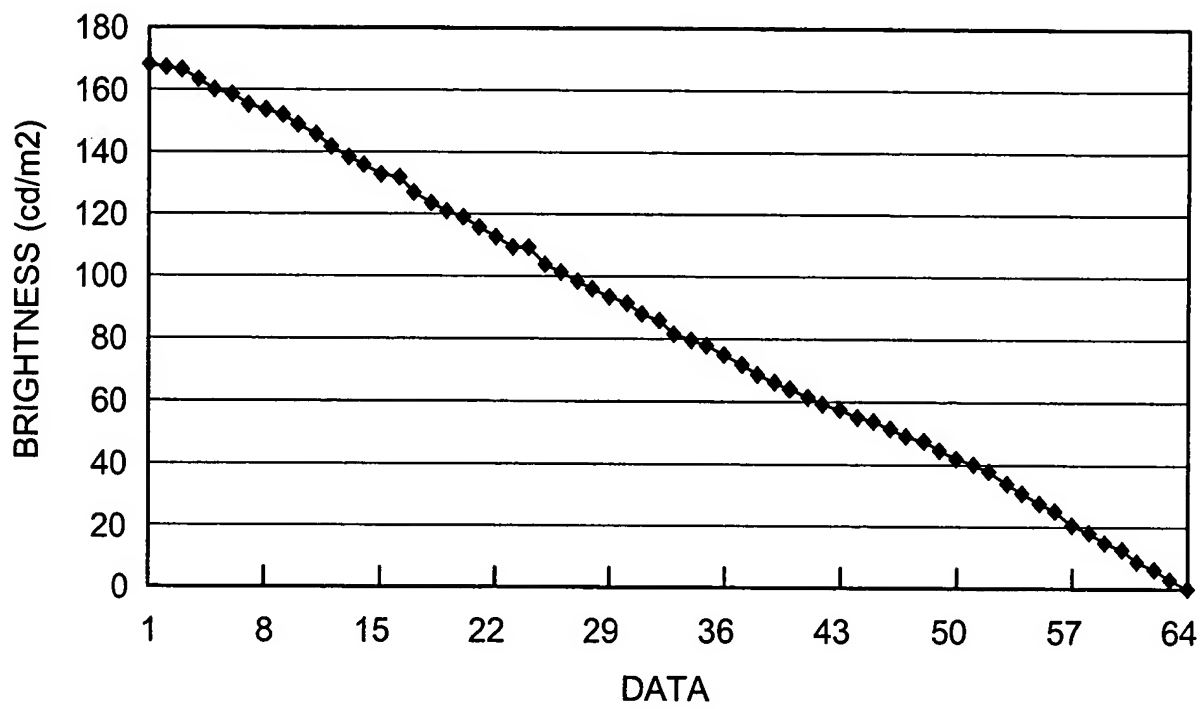


FIG. 45